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(52) UK CL (Edition Q)

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US 4728175 A

(58) Field of Search

UK CL (Edition Q) G5C CHE

INT CL⁶ G02F 1/1343 1/136

Online: EPODOC, JAPIO, WPI

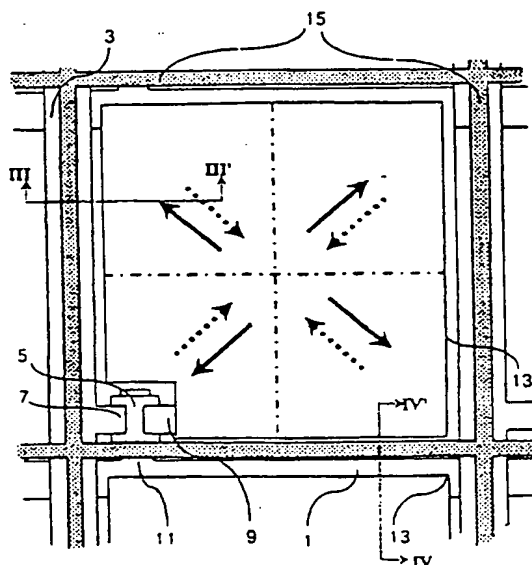
(54) Abstract Title

Liquid crystal display device with auxiliary electrode

(57) A liquid crystal display device includes a plurality of gate bus lines 1 arranged in a first direction, and a plurality of data bus lines 3 arranged in a second direction on the first substrate, to define a pixel region. A common electrode is formed on the second substrate and a pixel electrode 13 is electrically charged through the data bus line 3 to drive the liquid crystal layer with the common electrode. An auxiliary electrode 15 is formed on a same layer whereon the pixel electrode 13 is formed. A passivation is formed below the pixel electrode 13 and auxiliary electrode 15 to insulate them from other electrodes and bus lines. An alignment layer maybe on at least one substrate, and may comprise a plurality of different alignment domains.

In a preferred embodiment, the auxiliary electrode 15 is only formed along one edge of the pixel electrode (Fig 8A).

FIG. 6A



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FIG. 1A
PRIOR ART

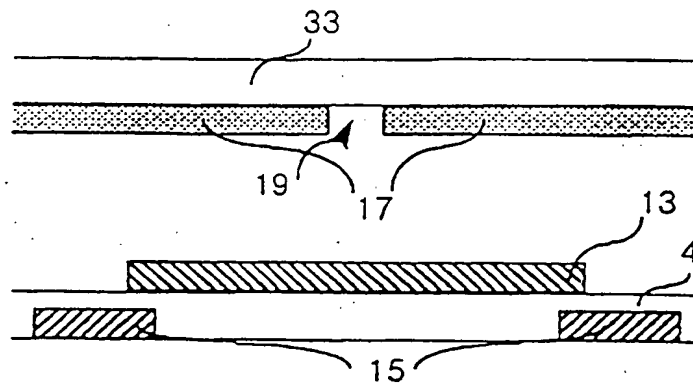


FIG. 1B
PRIOR ART

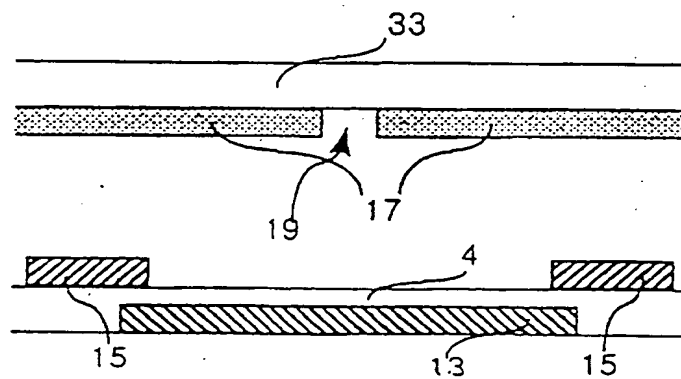


FIG. 1C
PRIOR ART

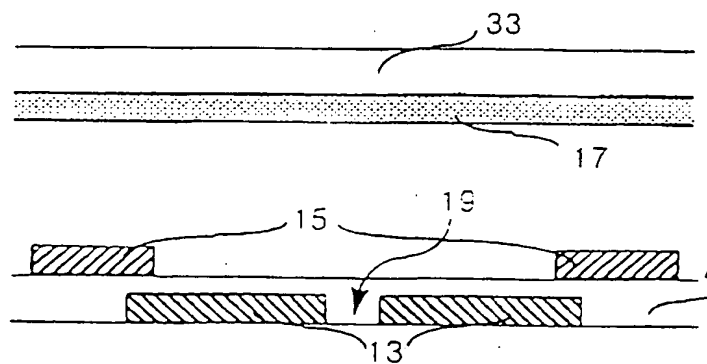
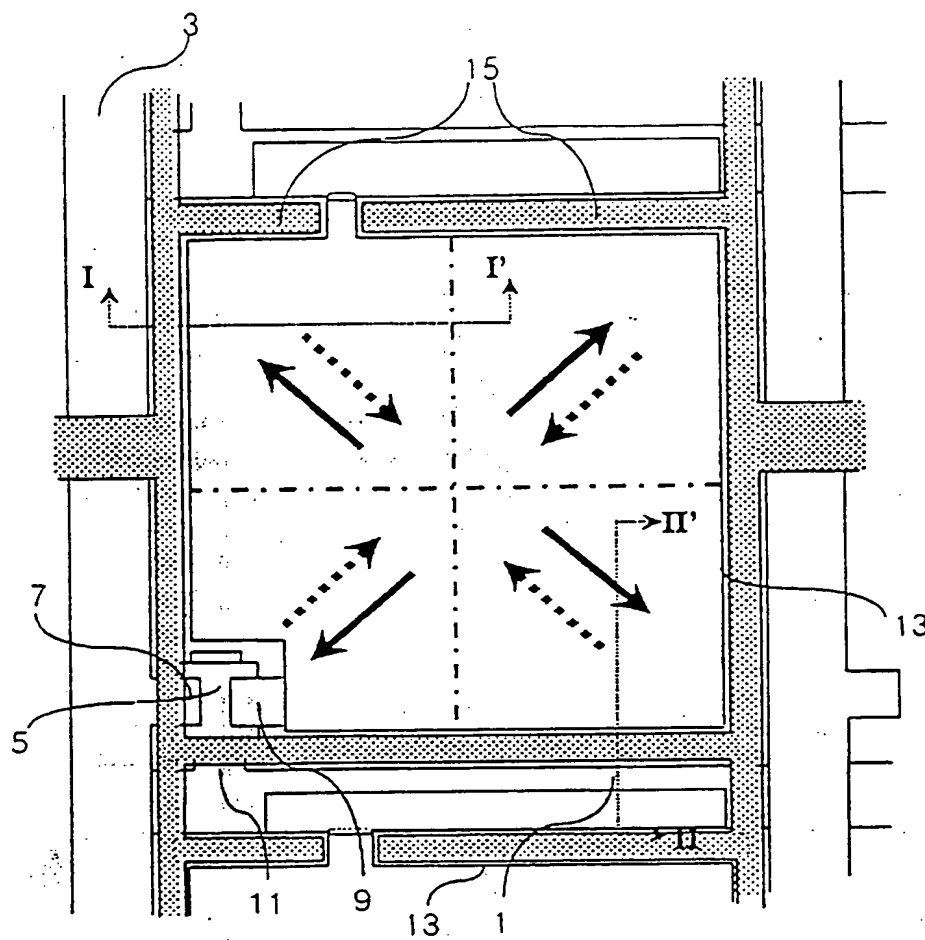
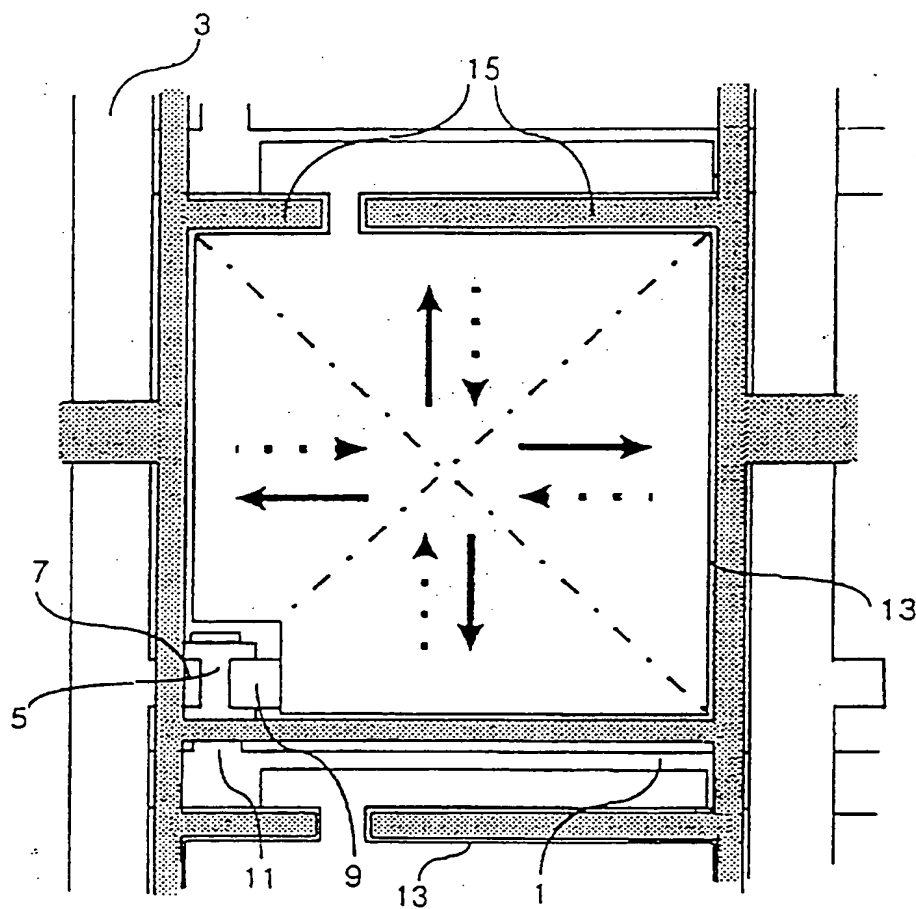


FIG. 2A



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FIG. 2B



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FIG. 2C

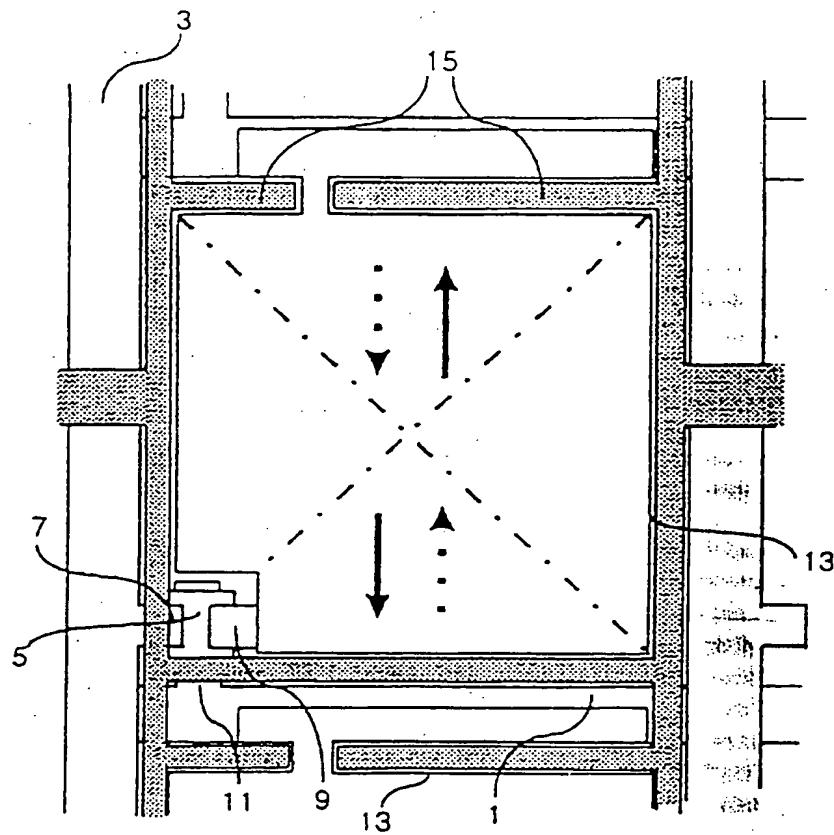
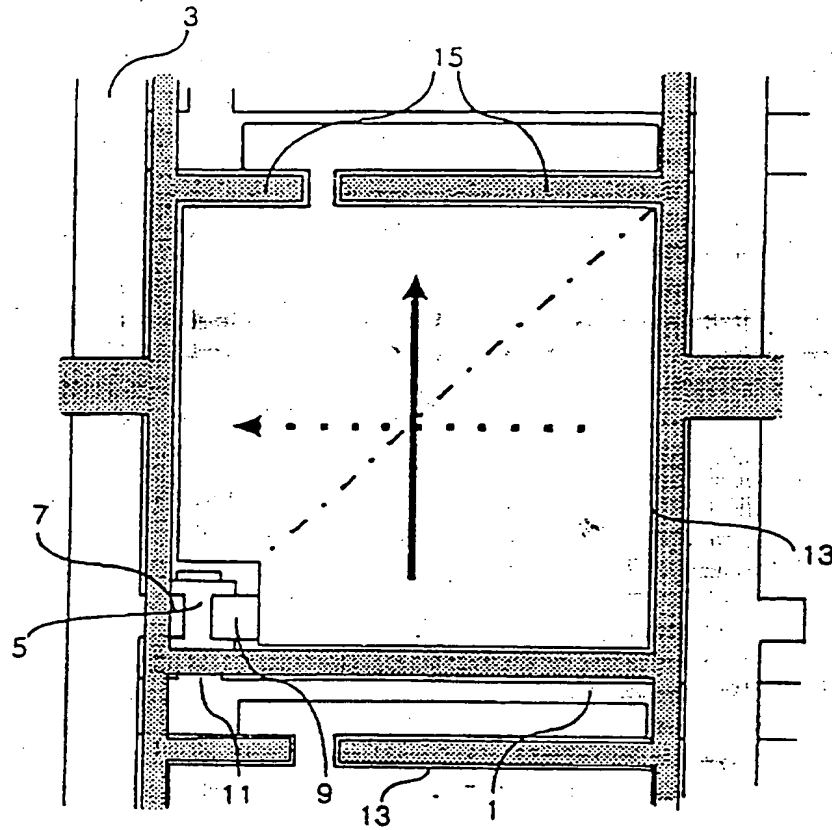


FIG. 2D



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FIG. 3A

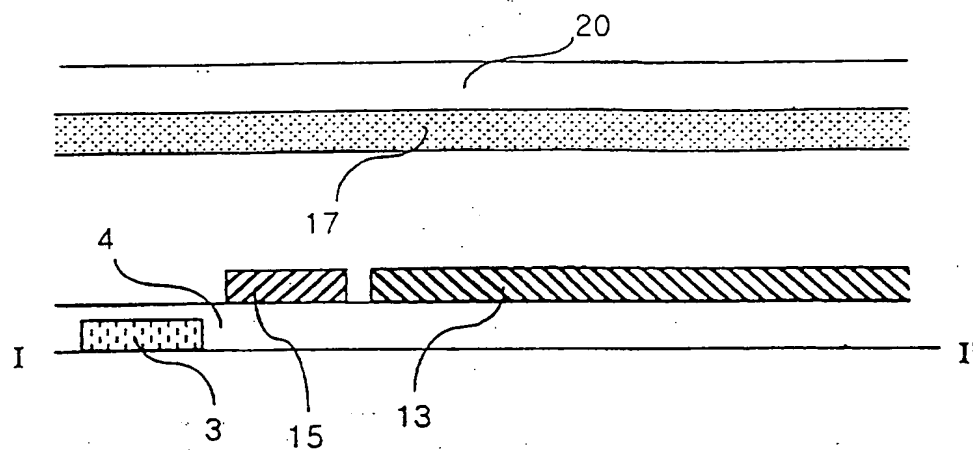
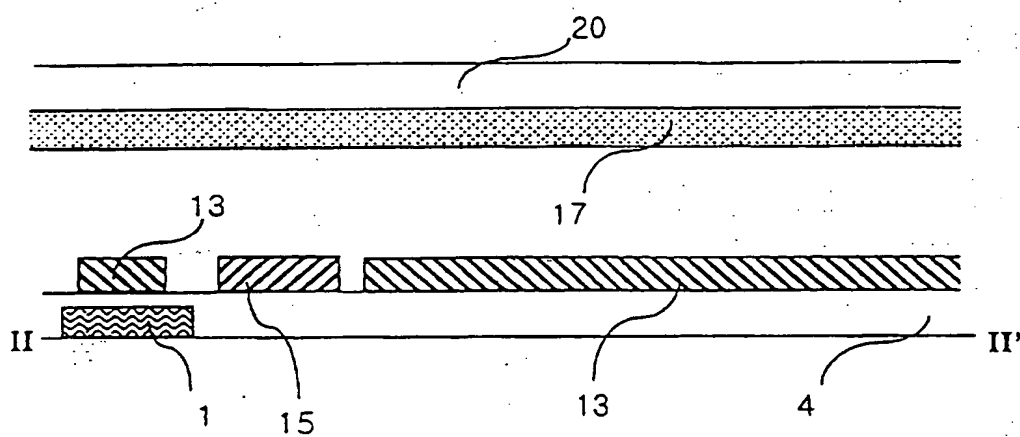
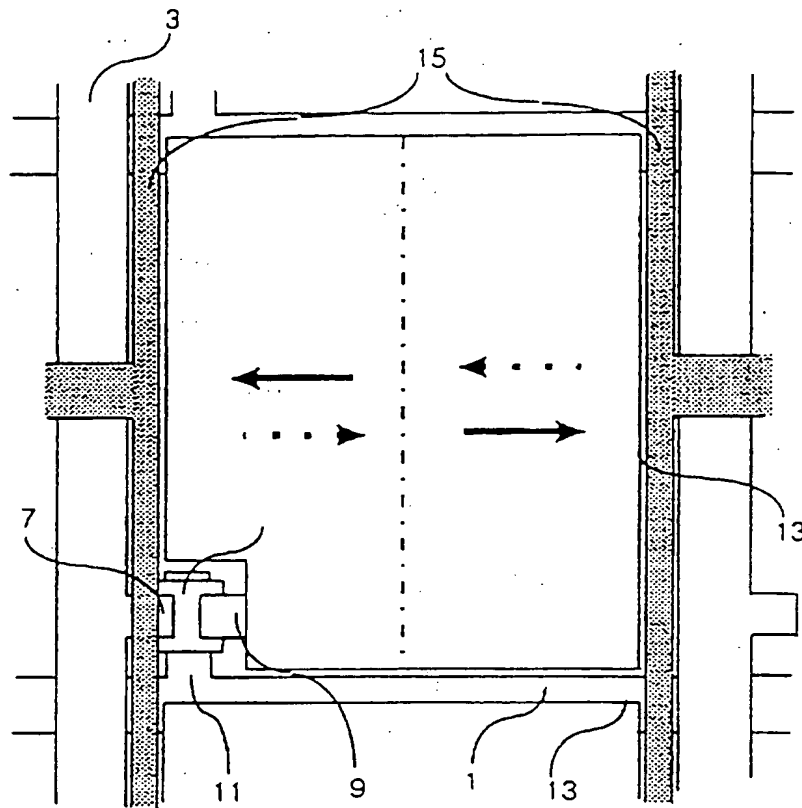


FIG. 3B



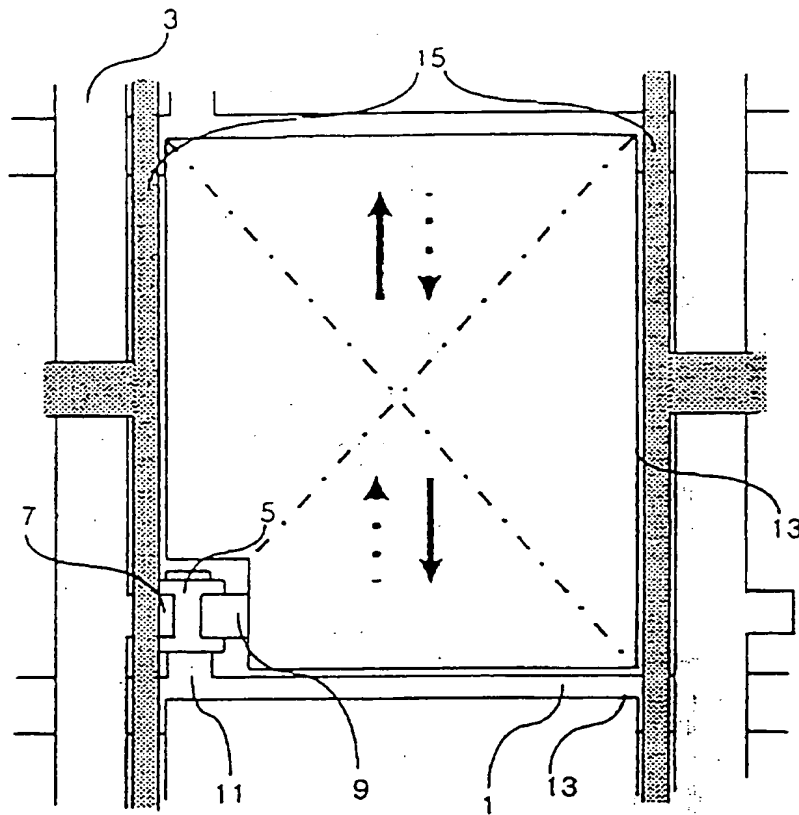
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FIG. 4A



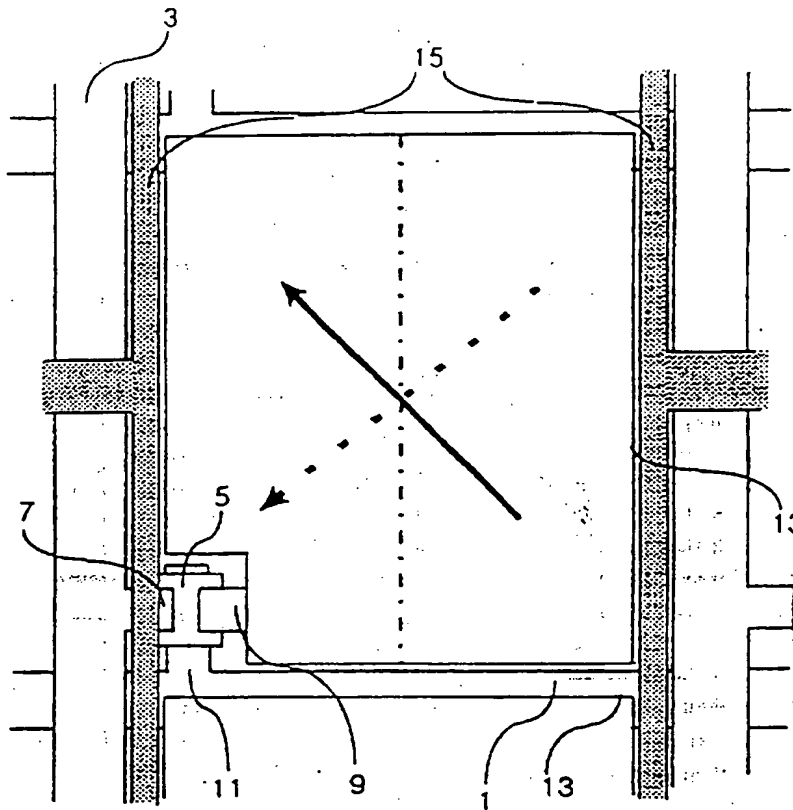
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FIG. 4B



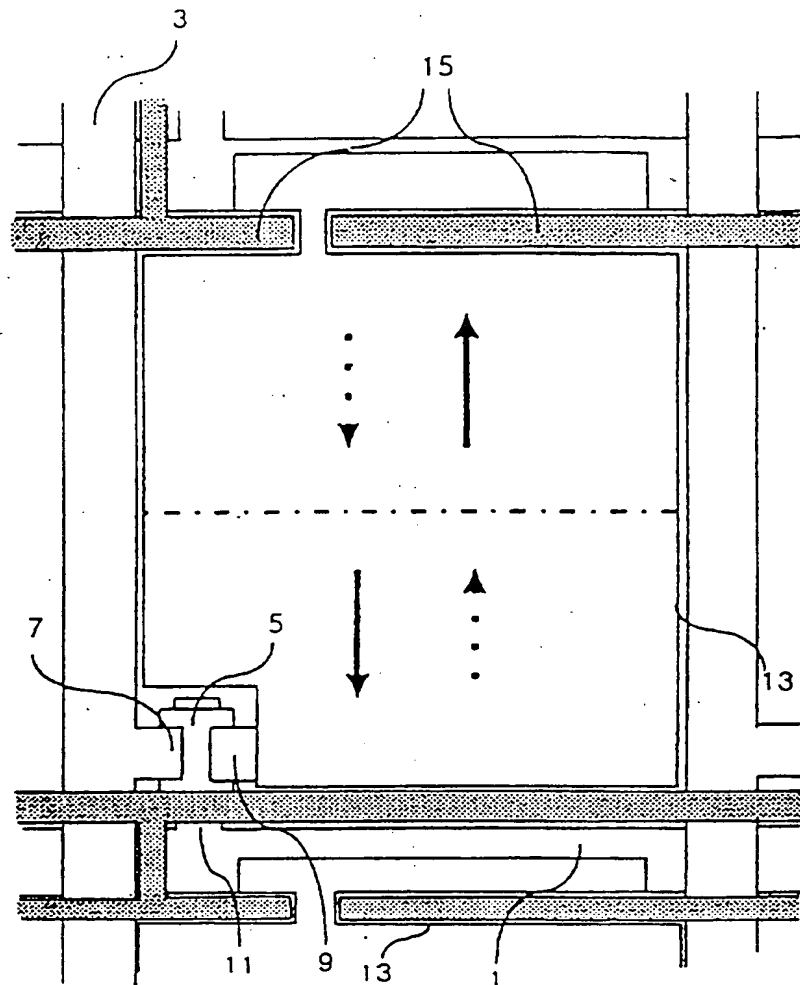
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FIG. 4C



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FIG. 5A



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FIG. 5B

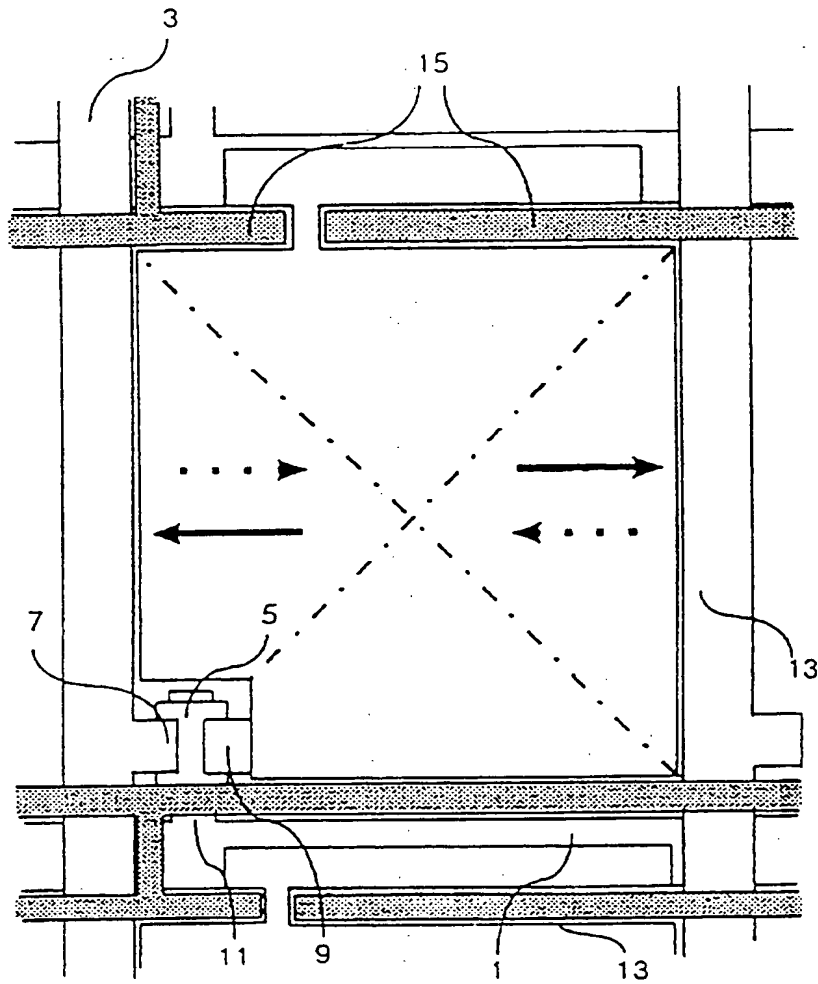
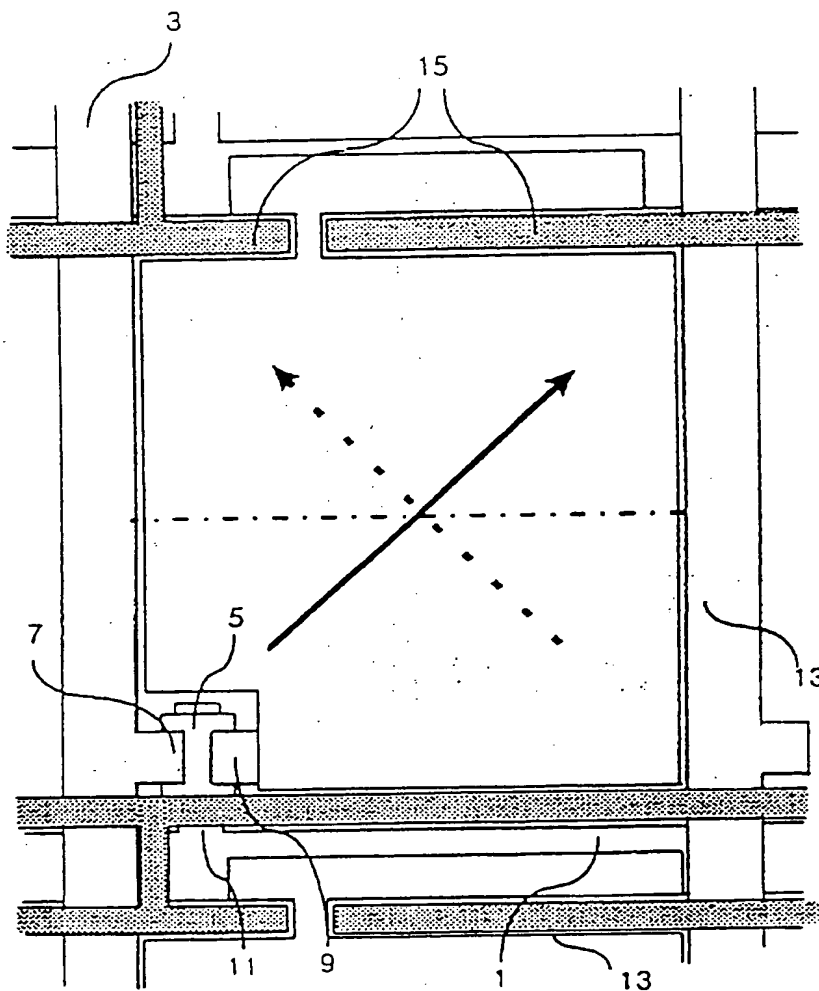


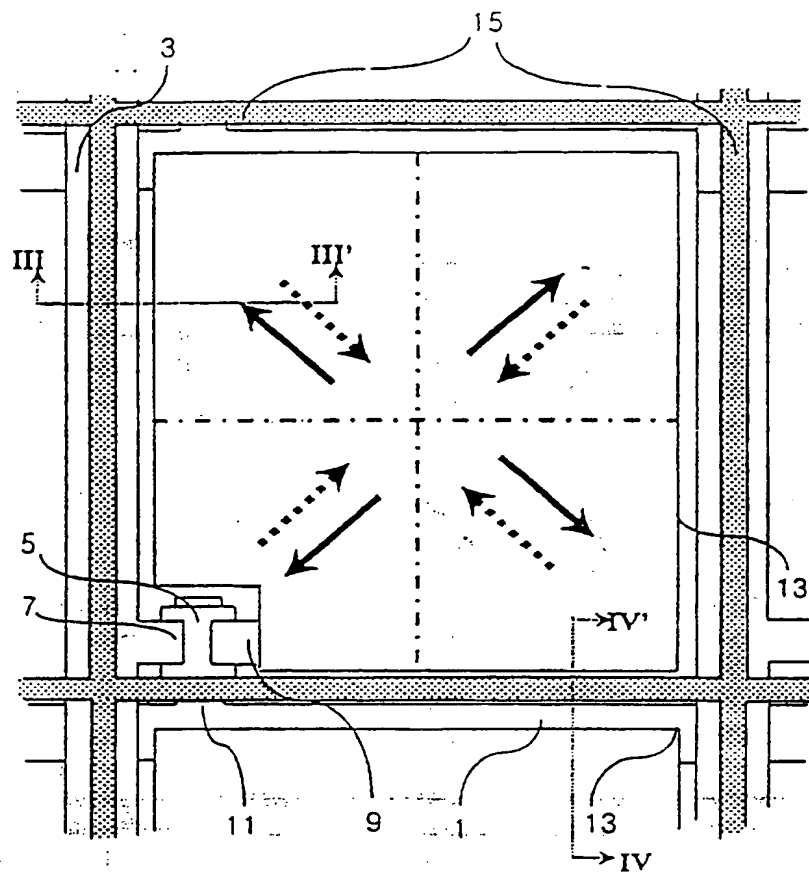
FIG. 5C

FIG. 5C



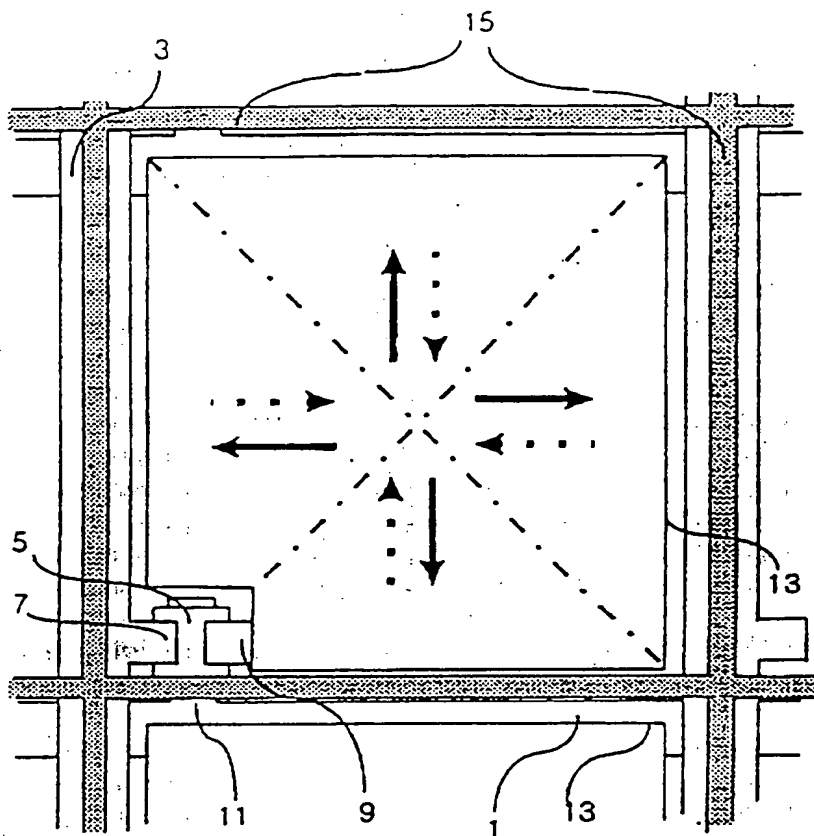
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FIG. 6A



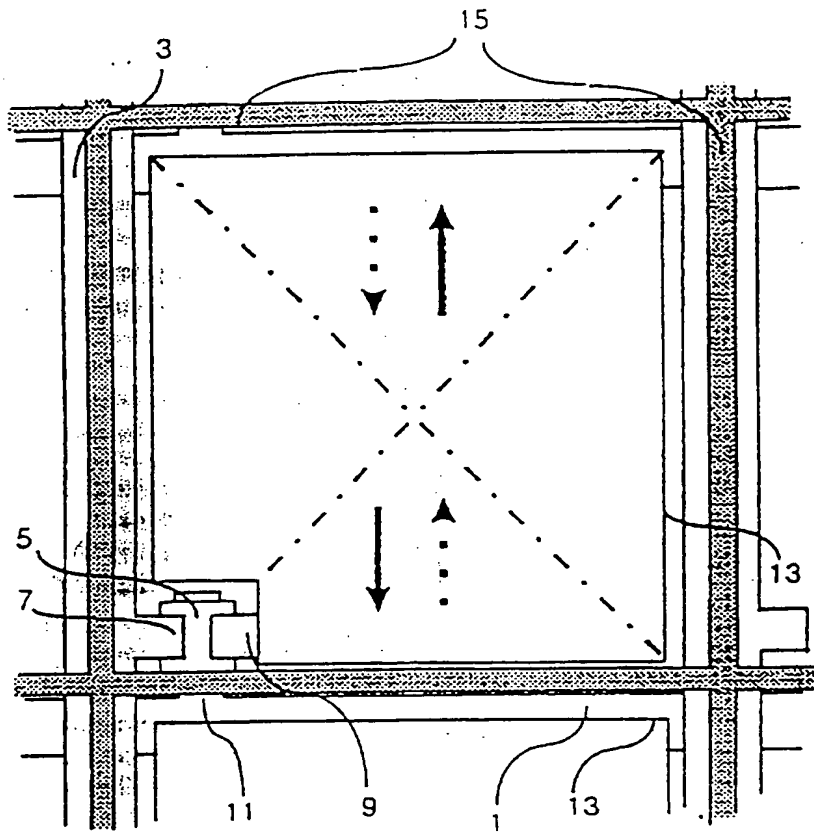
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FIG. 6B



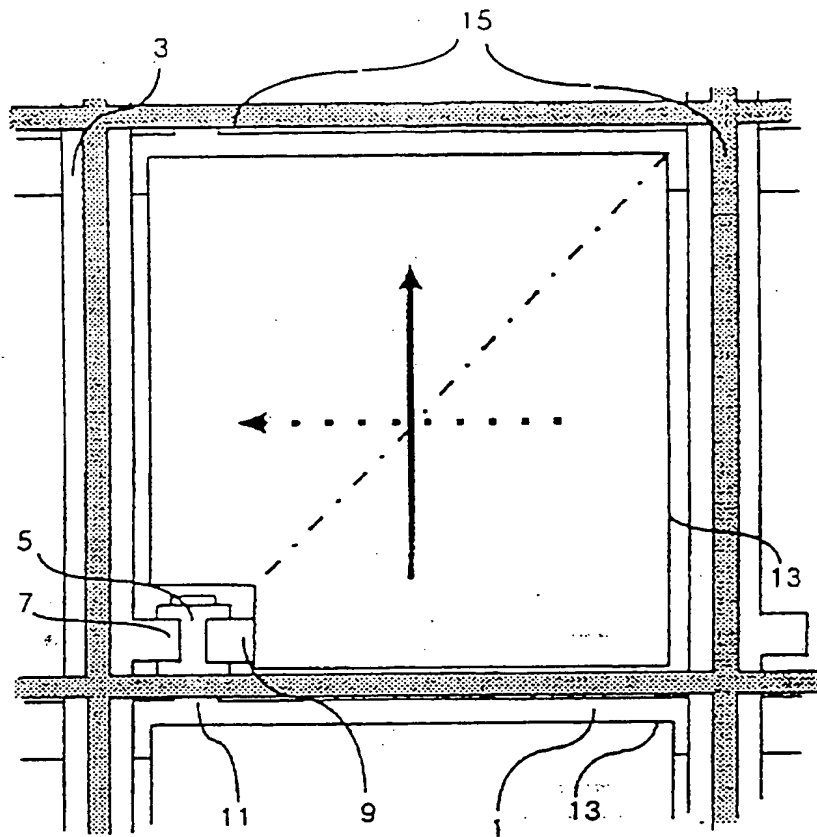
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FIG. 6C



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FIG. 6D



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FIG. 7A

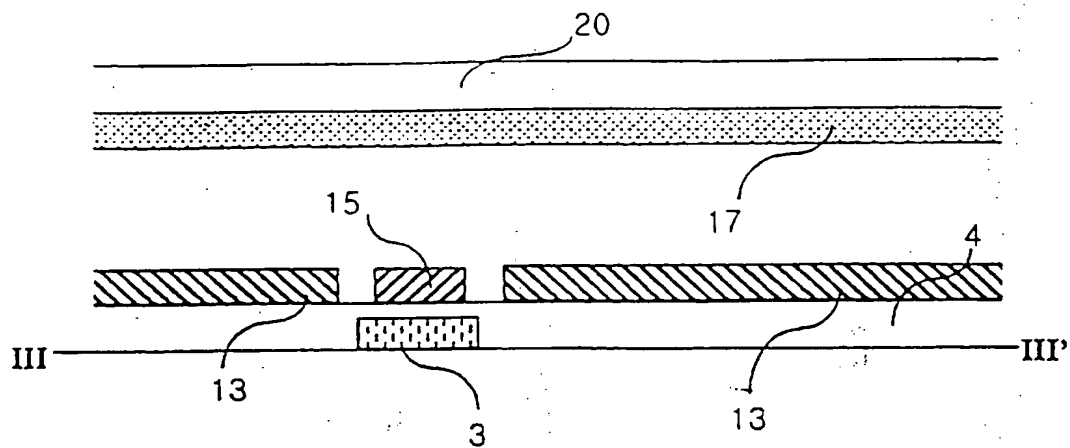
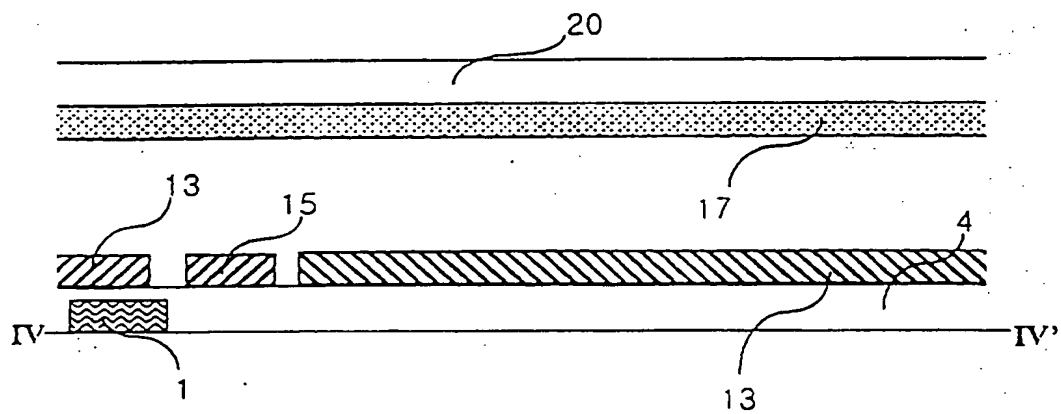
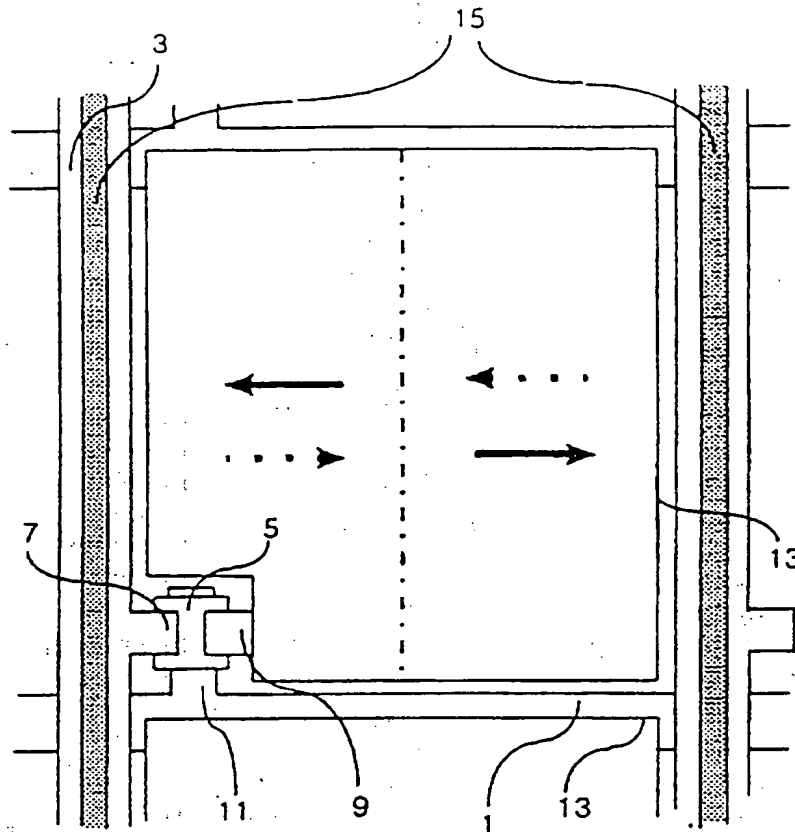


FIG. 7B



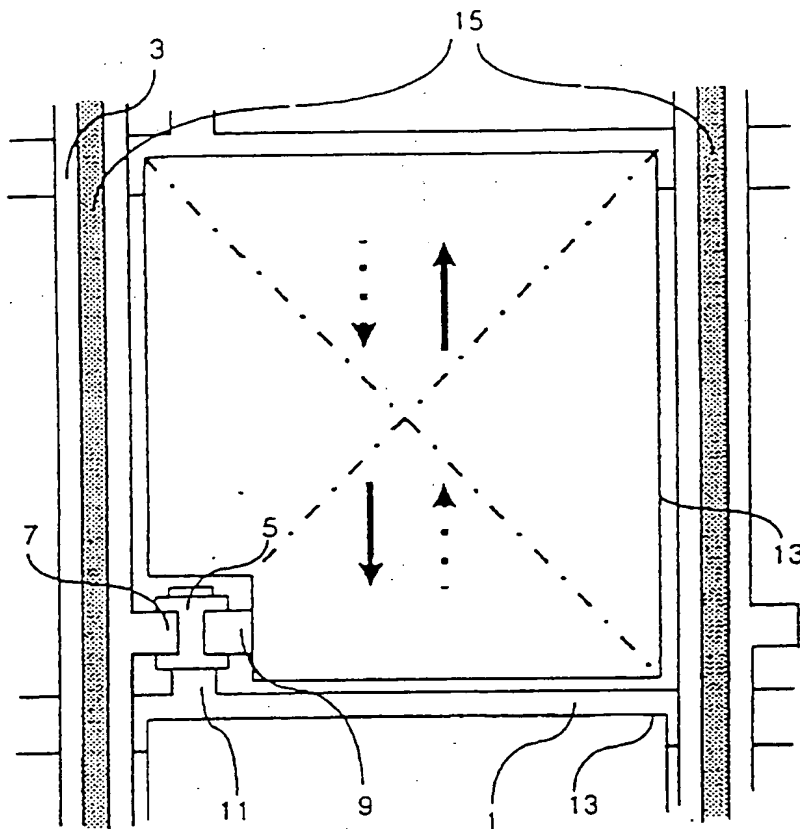
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FIG. 8A



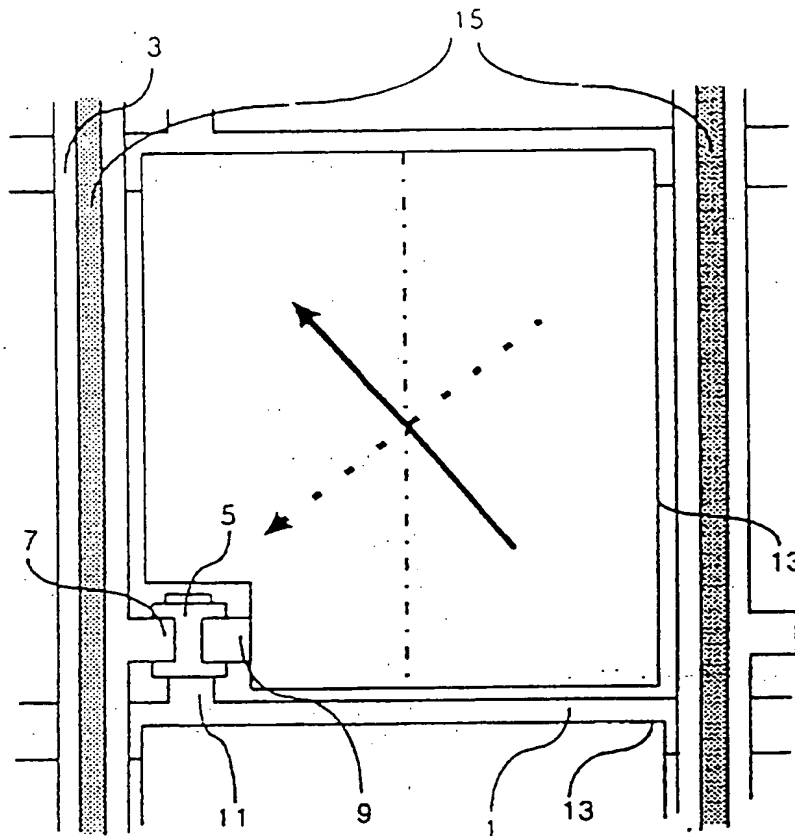
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FIG. 8B



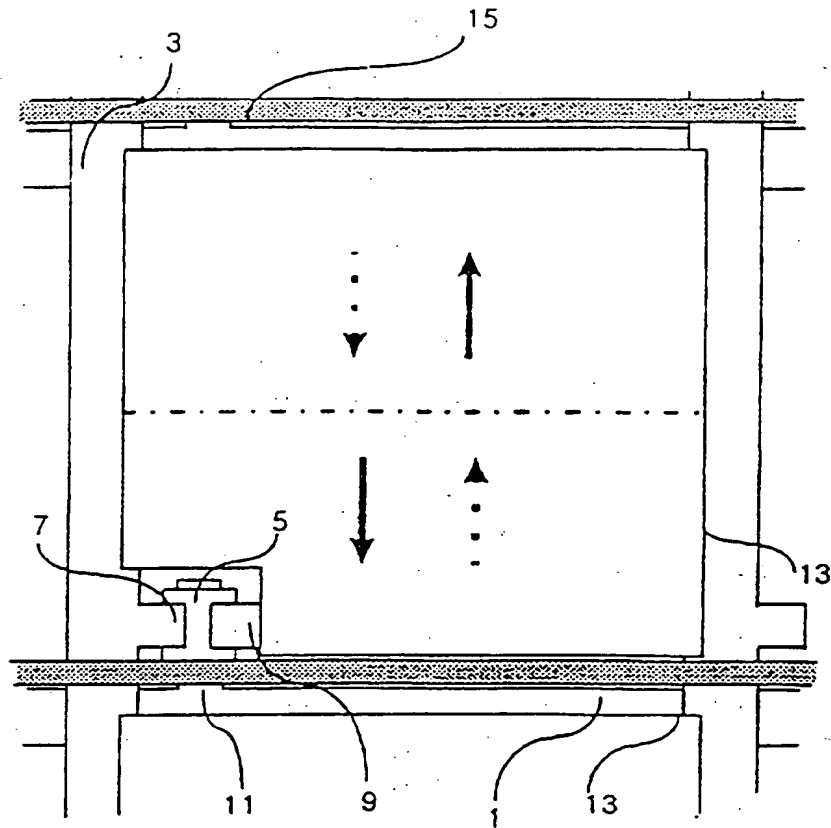
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FIG. 8C



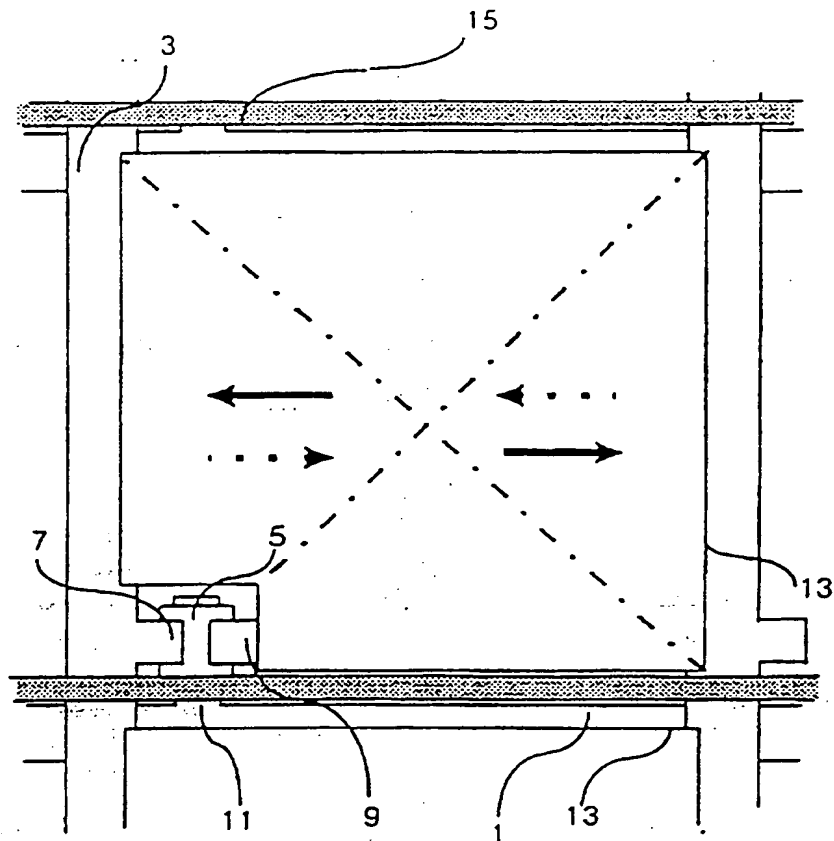
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FIG. 9A



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FIG. 9B



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FIG. 9C

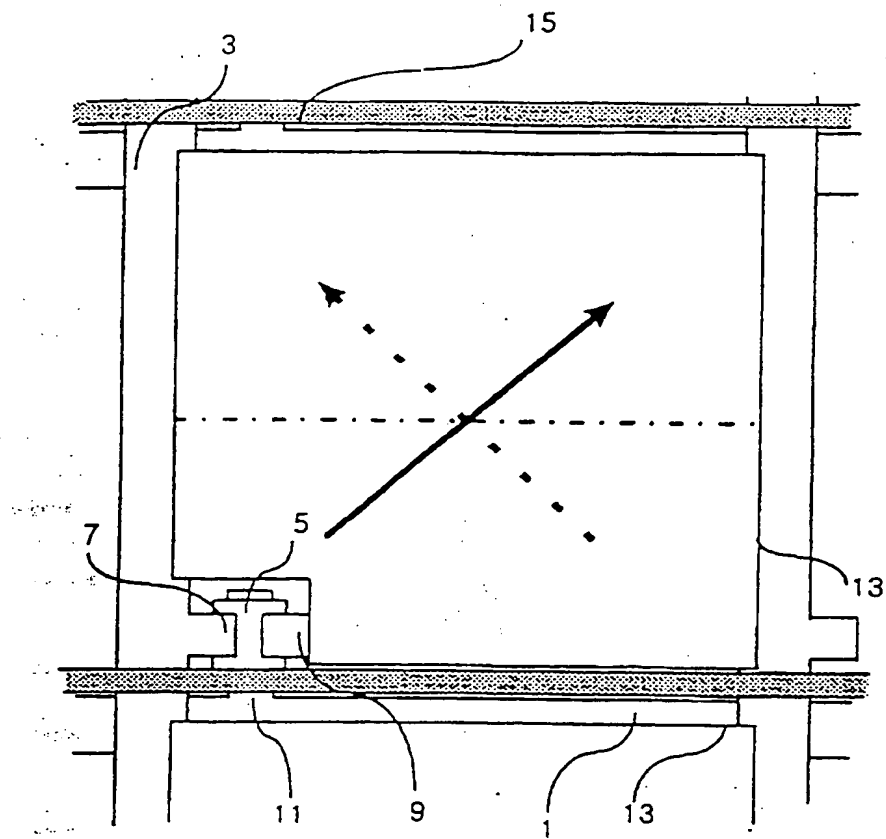
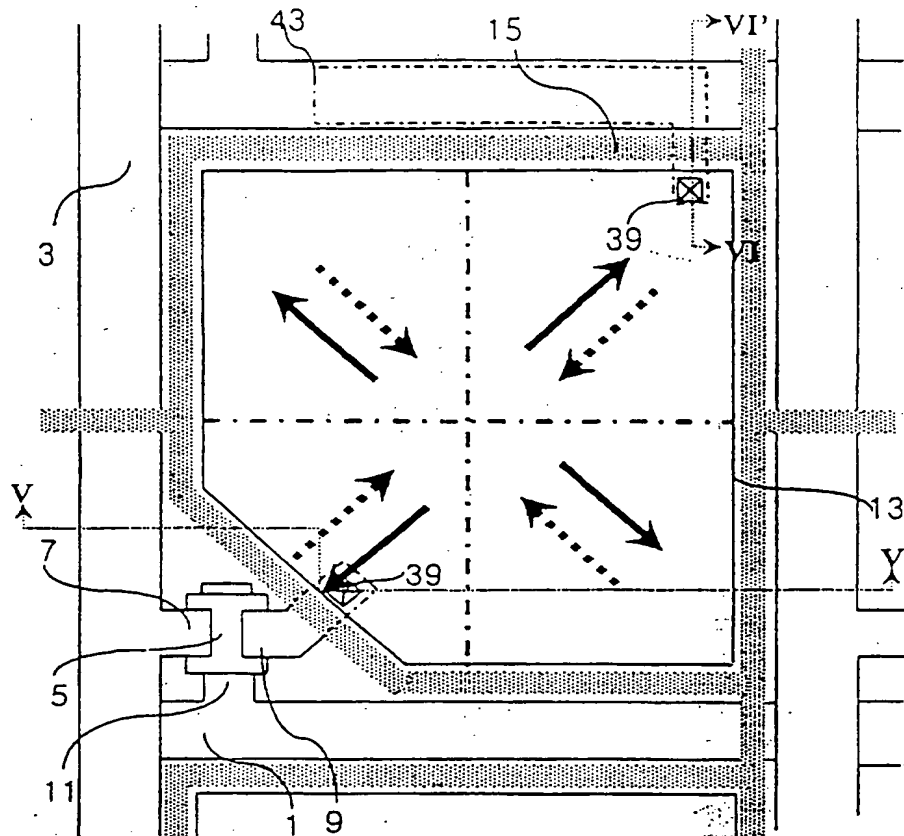
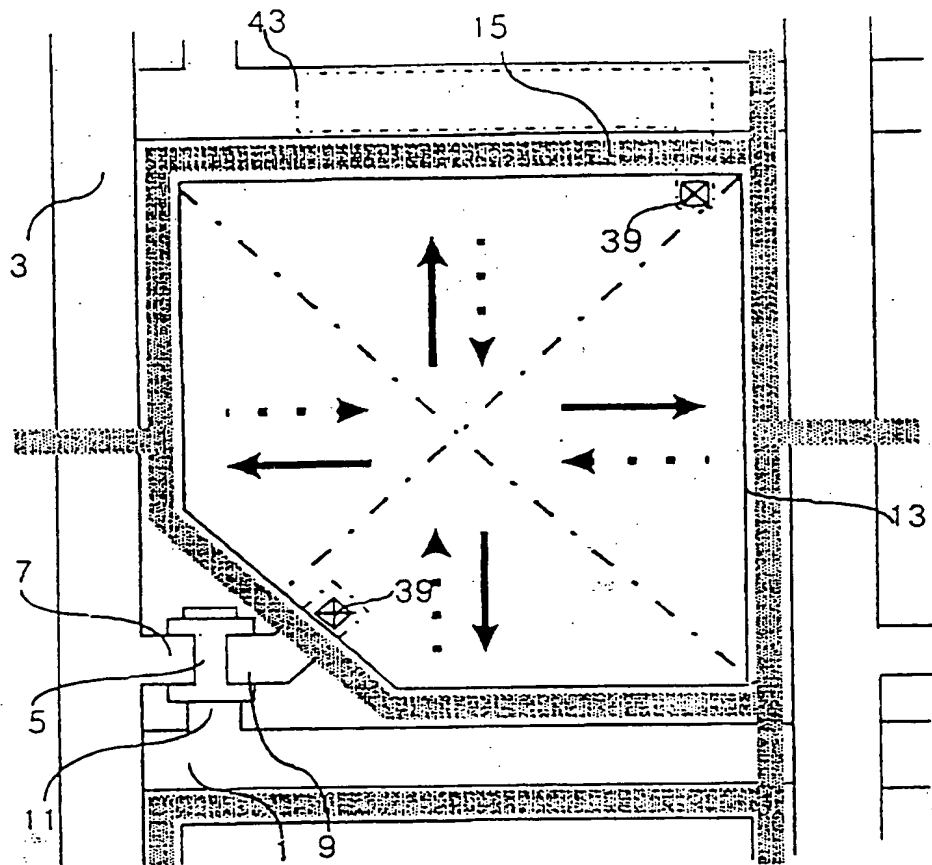


FIG. 10A



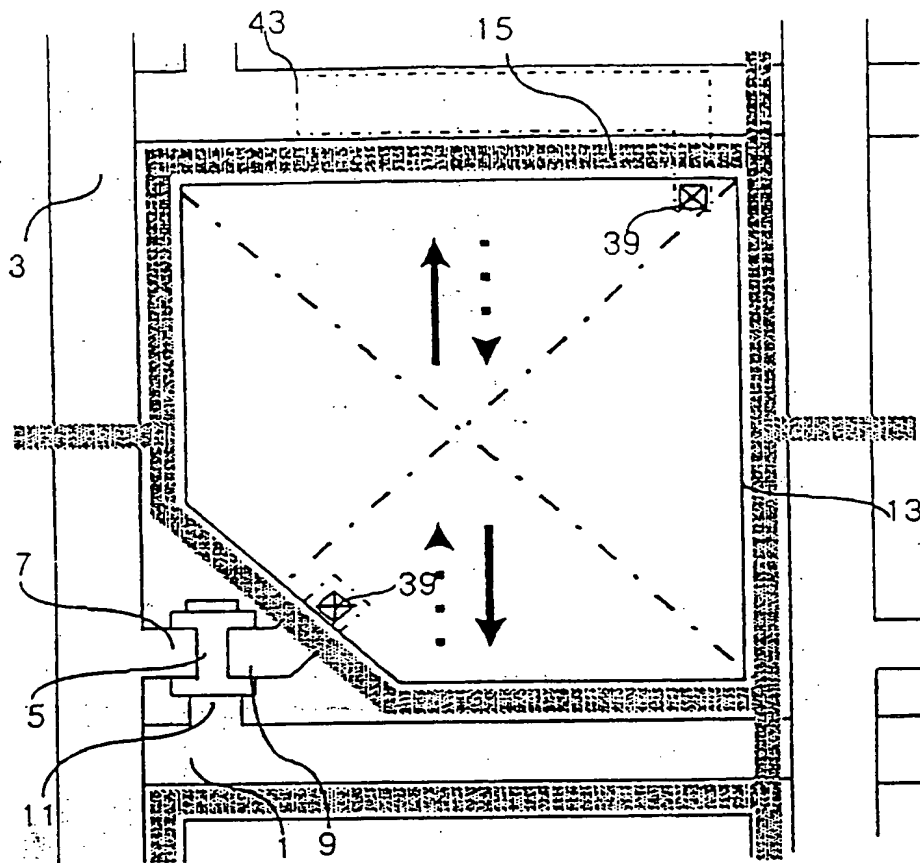
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FIG. 10B



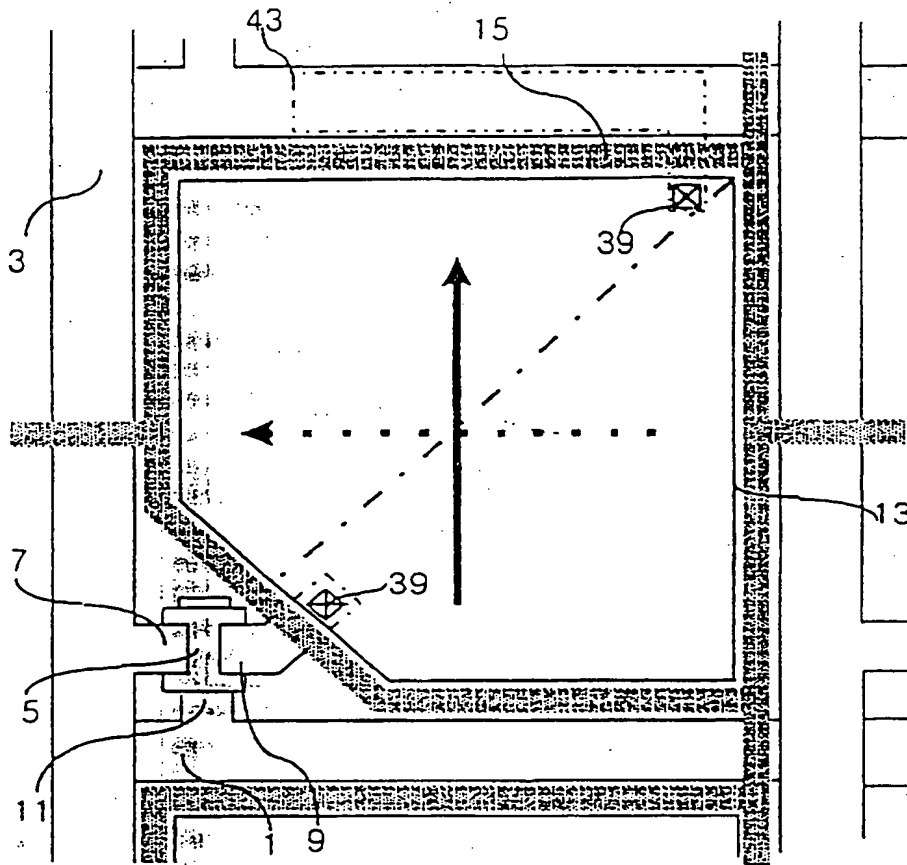
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FIG. 10C



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FIG. 10D



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FIG. 11A

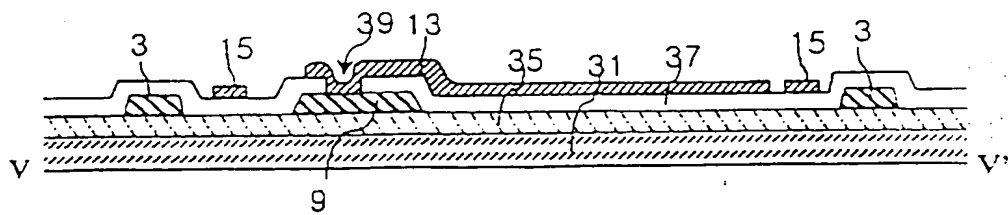
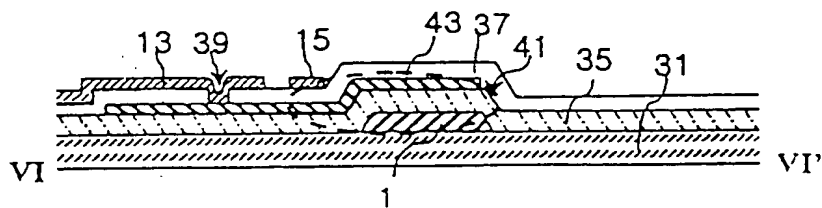


FIG. 11B



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FIG. 12A

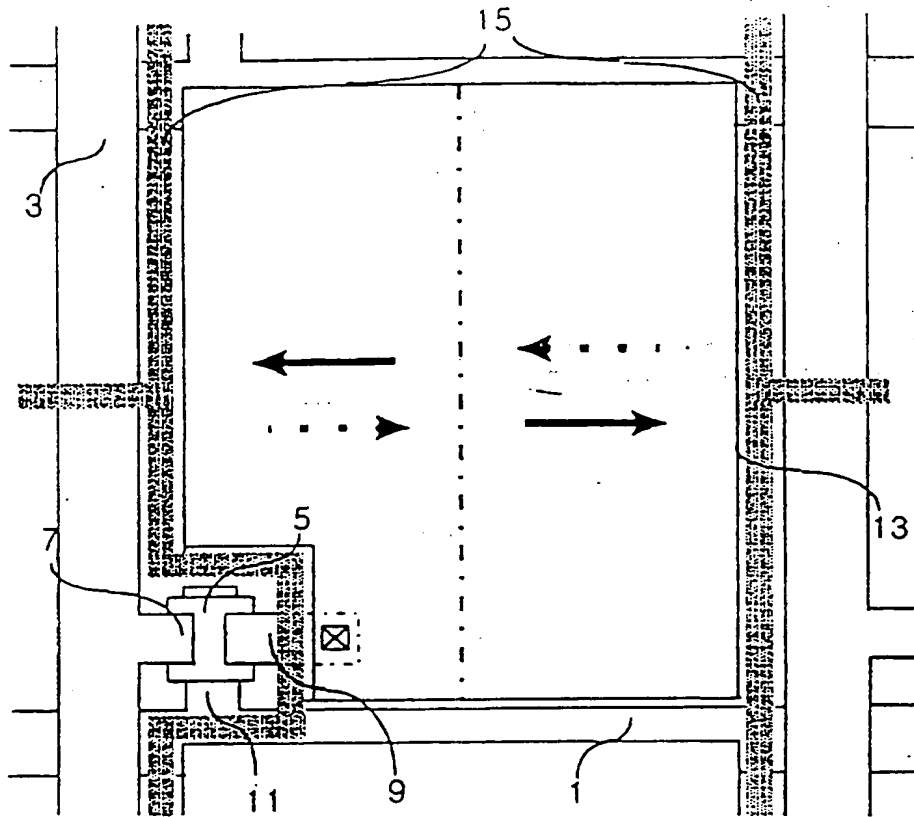
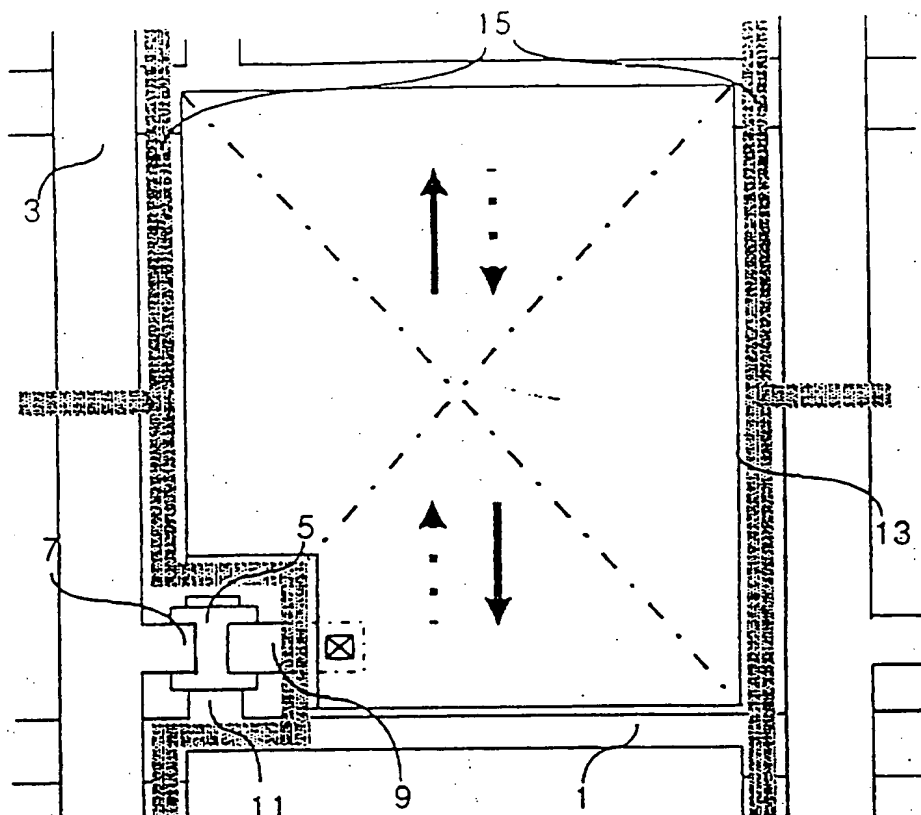
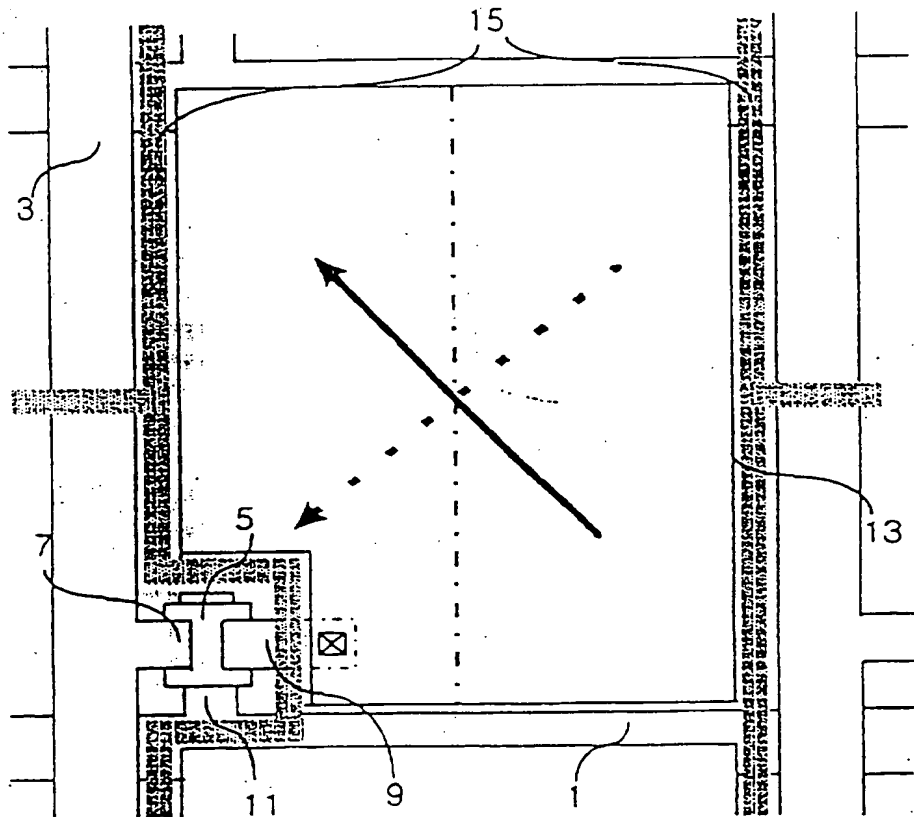


FIG. 128



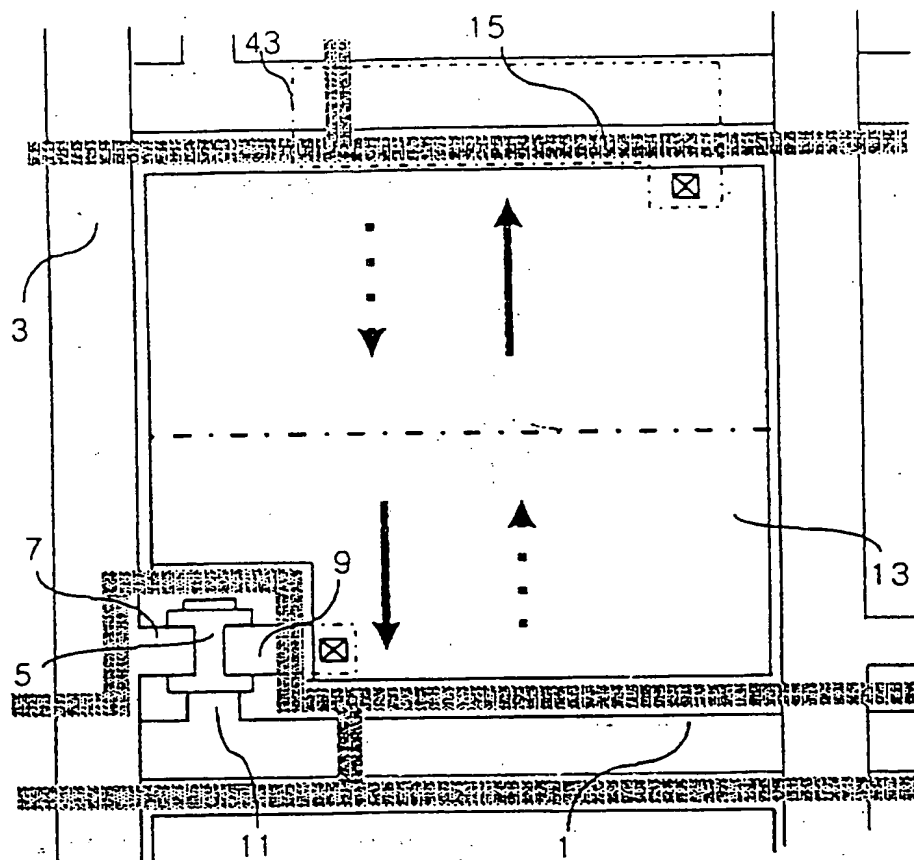
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FIG. 12C



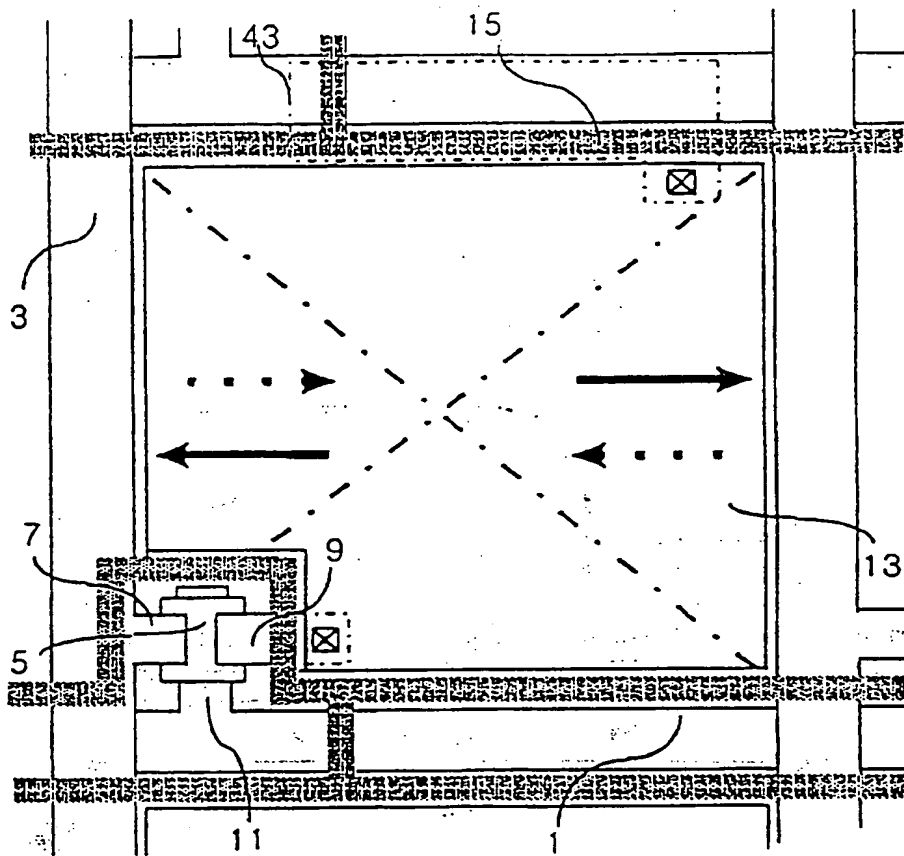
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FIG. 13A



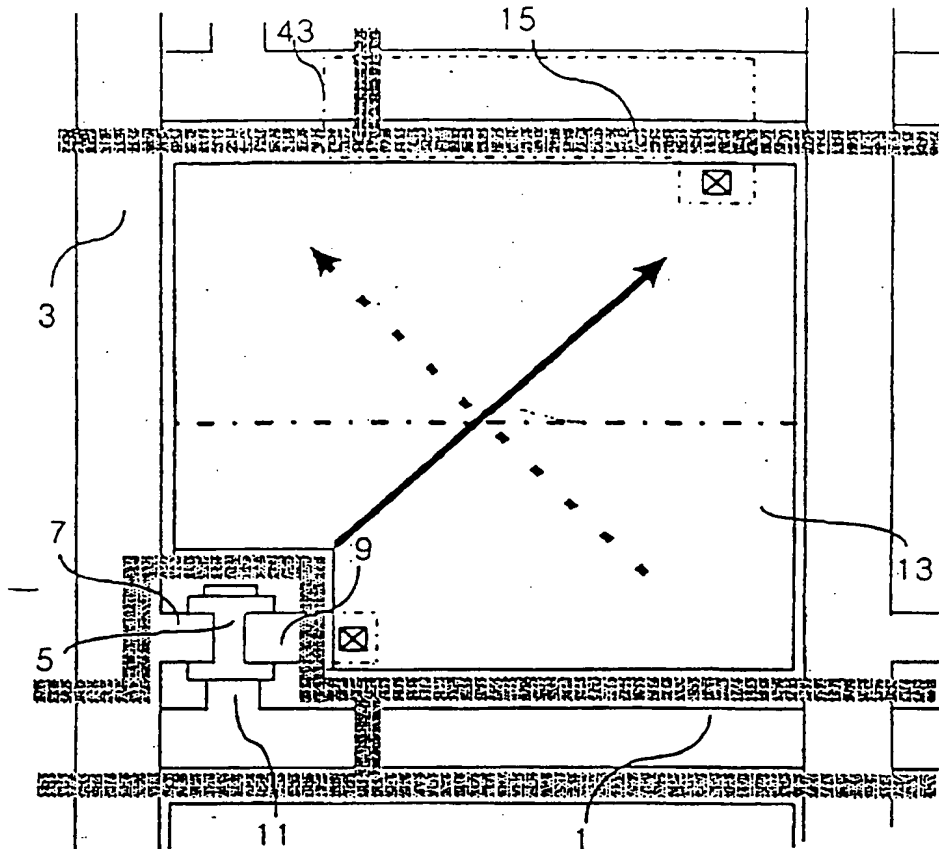
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FIG. 13B



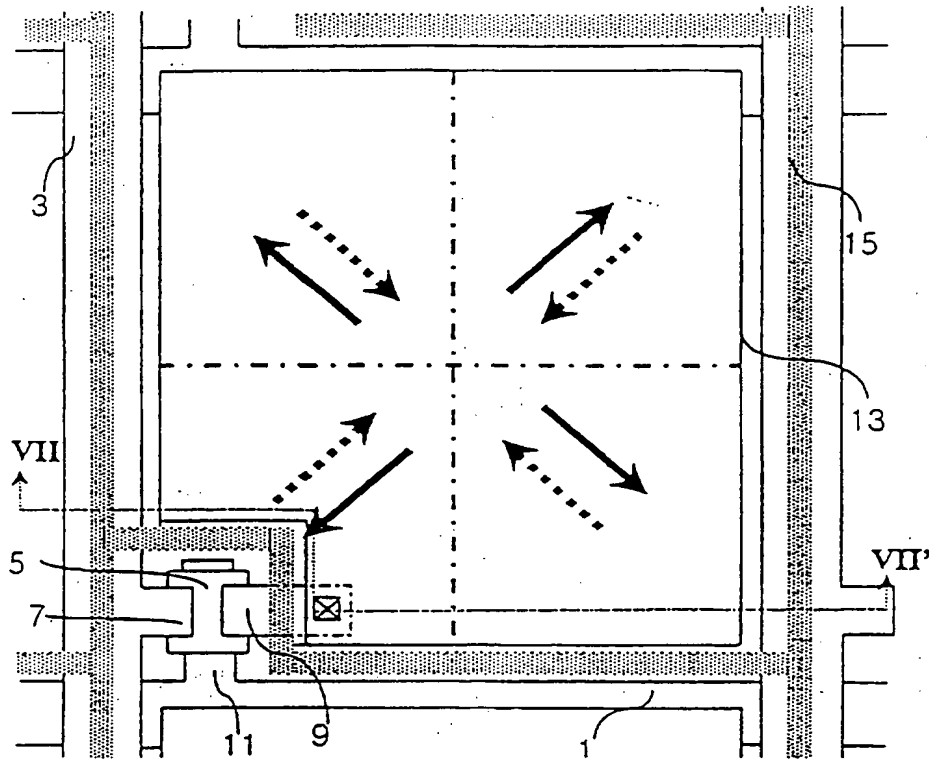
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FIG. 13C



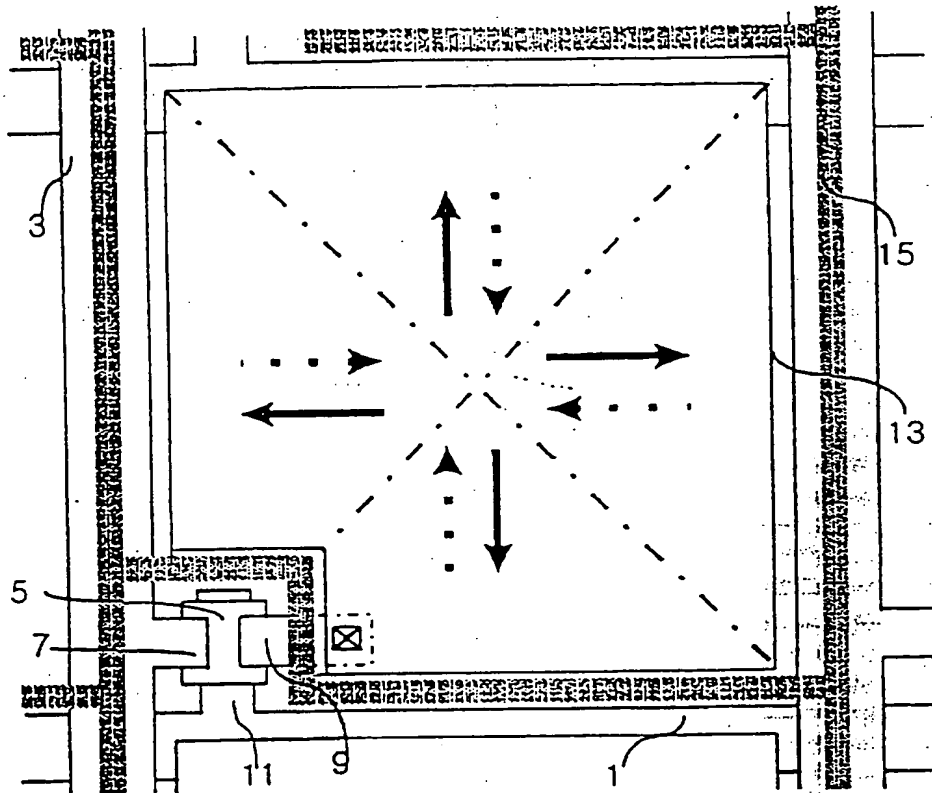
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FIG. 14A



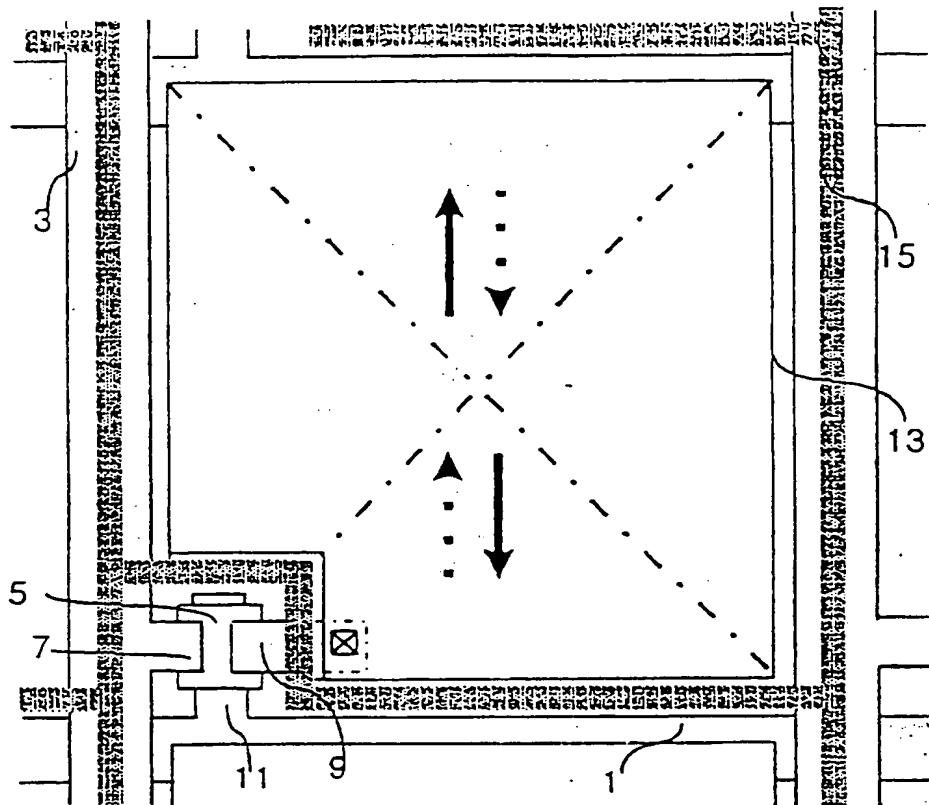
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FIG. 14B



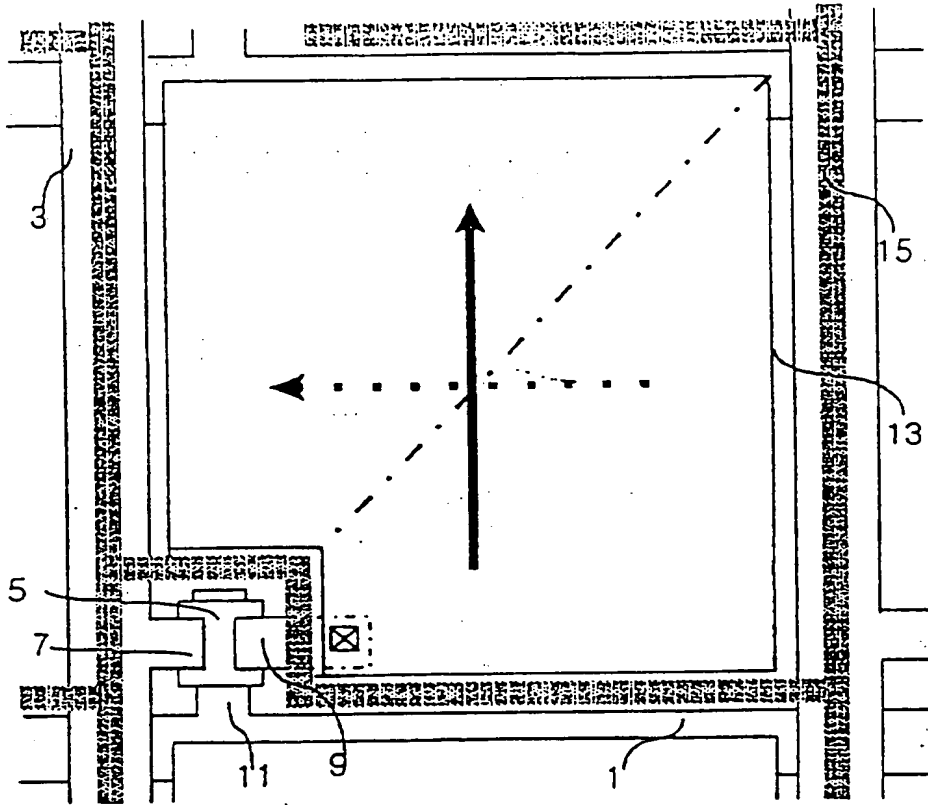
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FIG. 14C



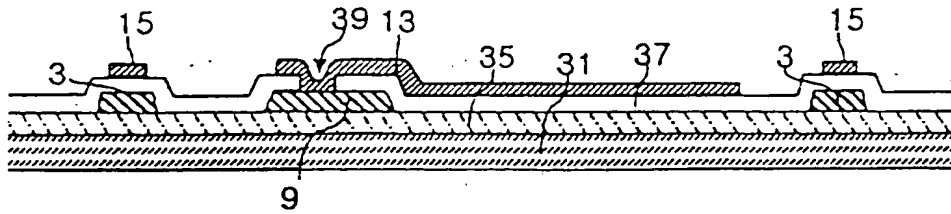
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FIG. 14D



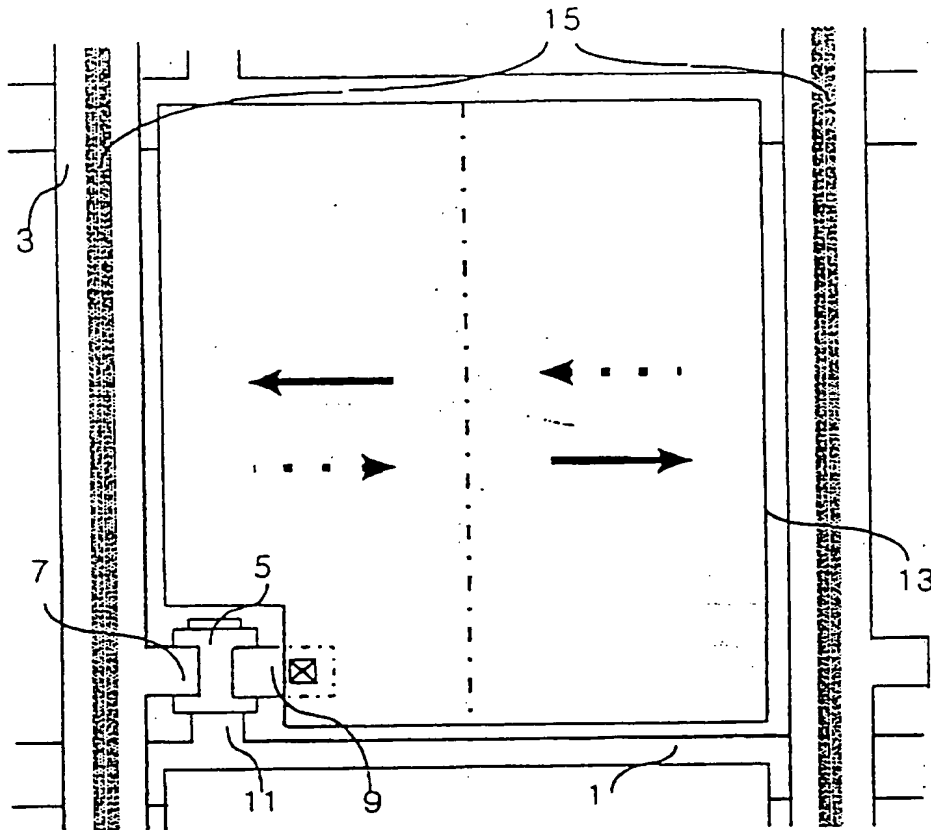
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FIG. 15



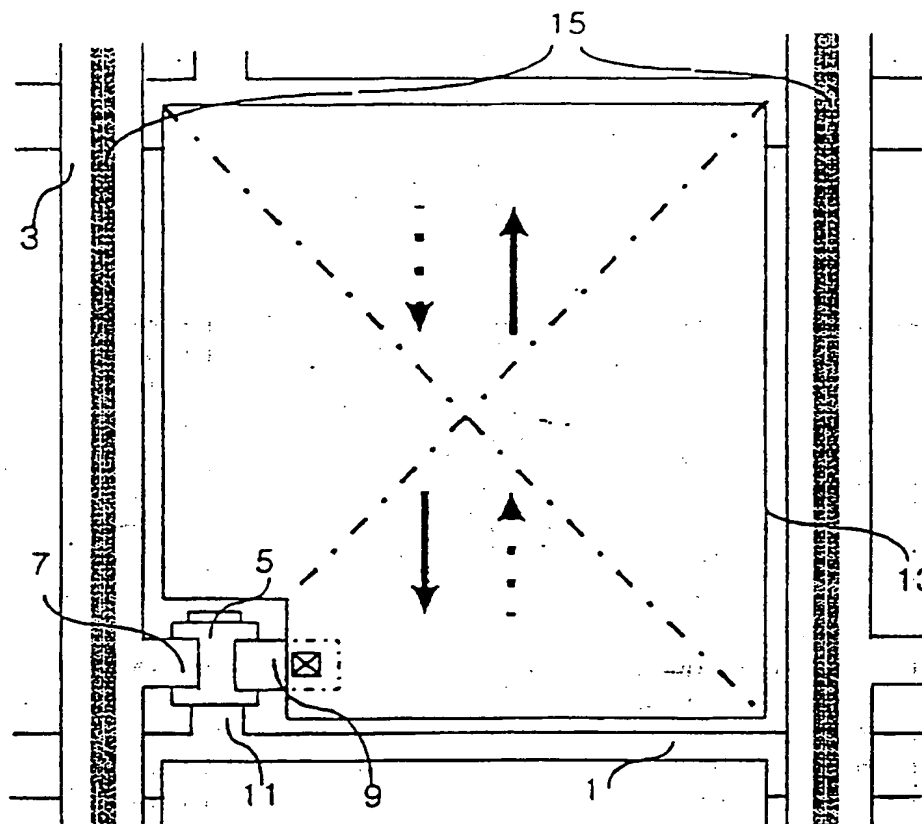
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FIG. 16A



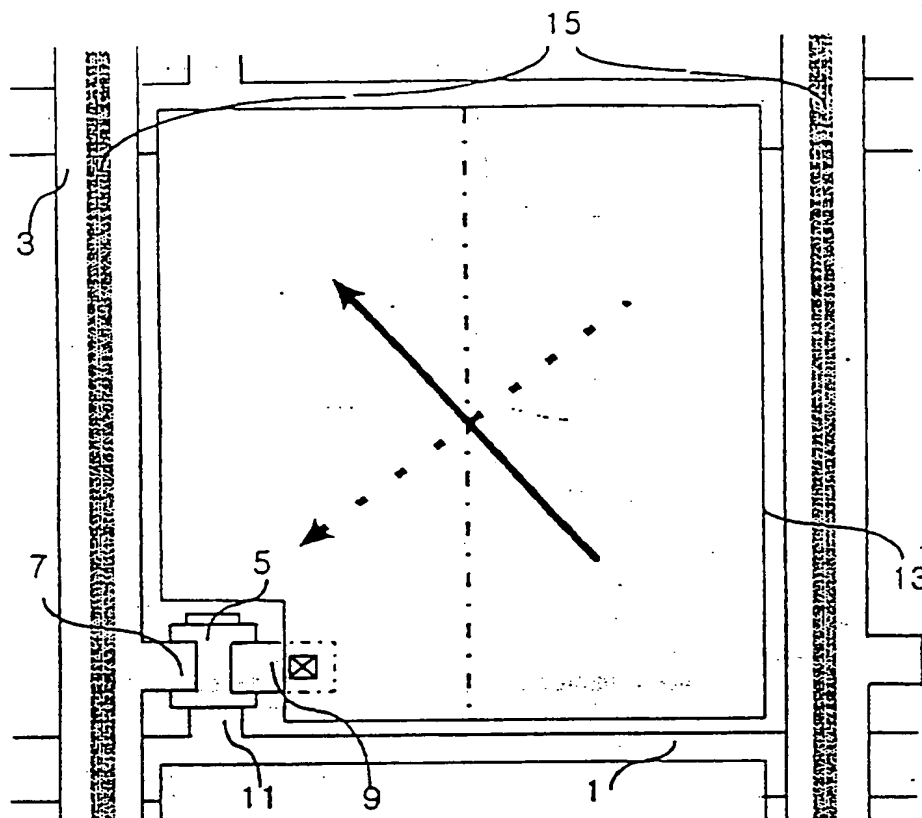
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FIG. 16B



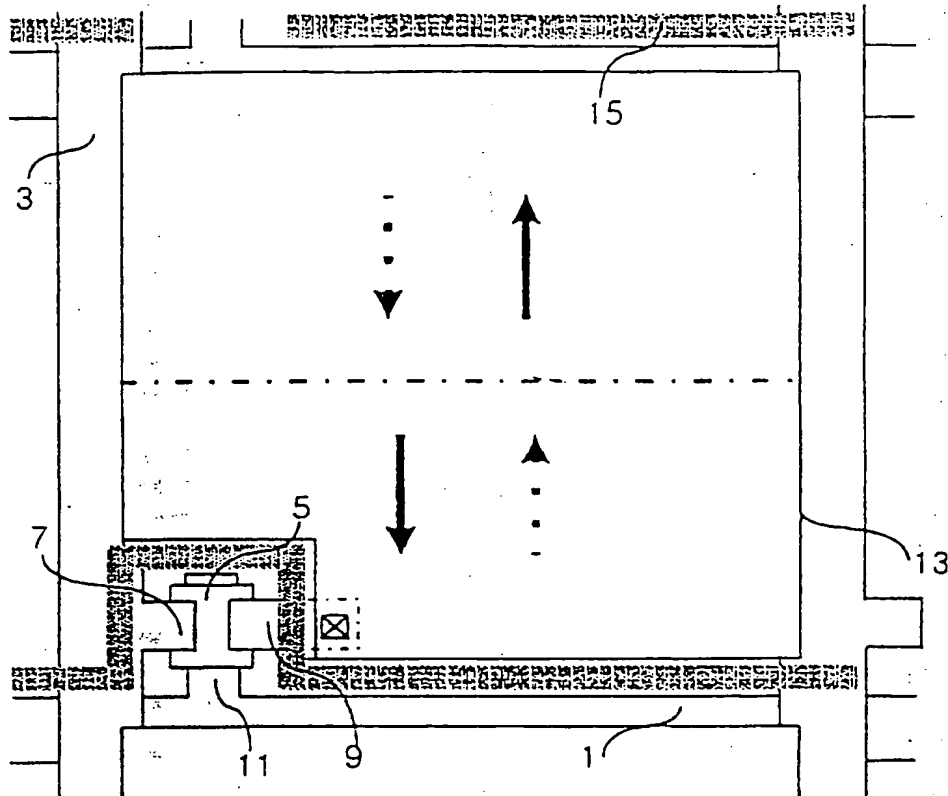
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FIG. 16C



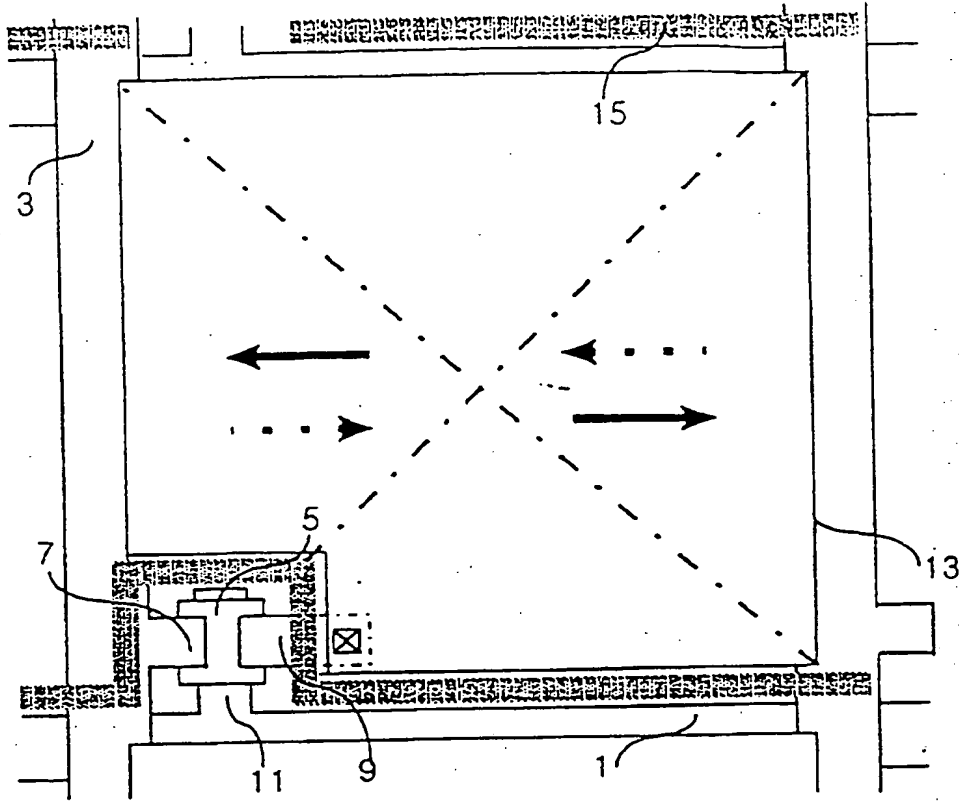
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FIG. 17A



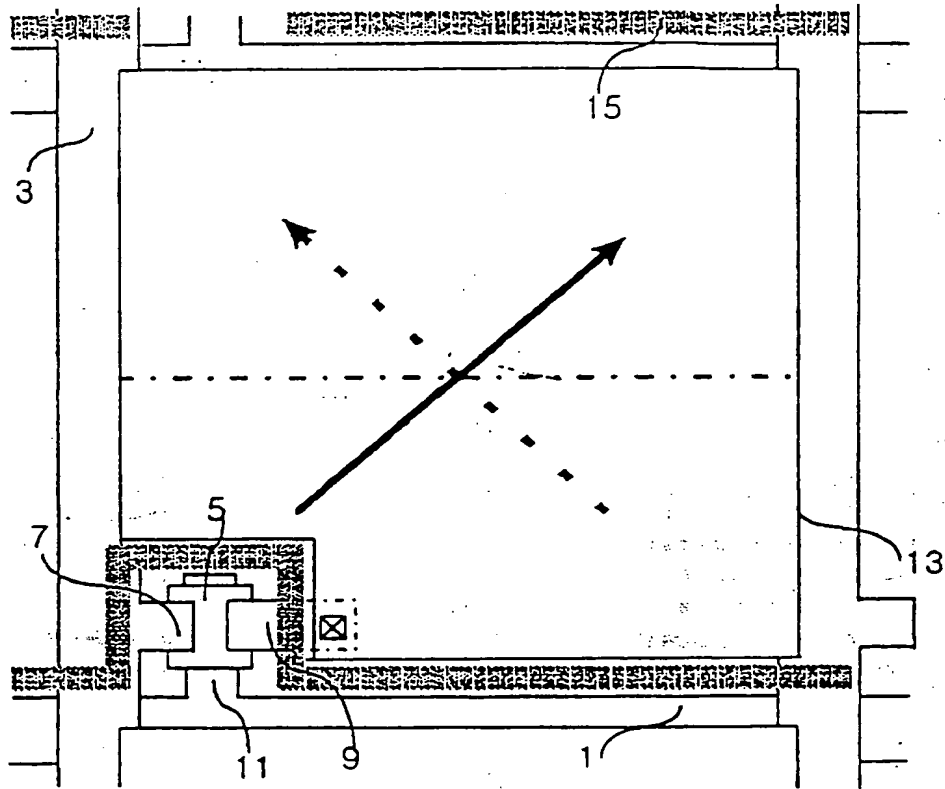
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FIG. 17B



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FIG. 17C



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FIG. 18A

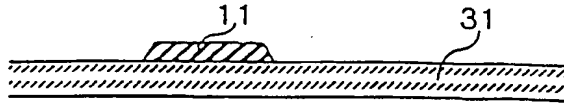


FIG. 18B

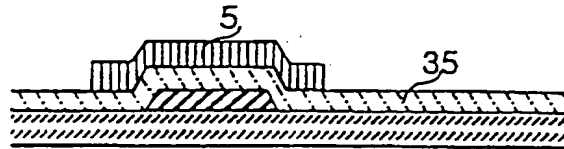


FIG. 18C

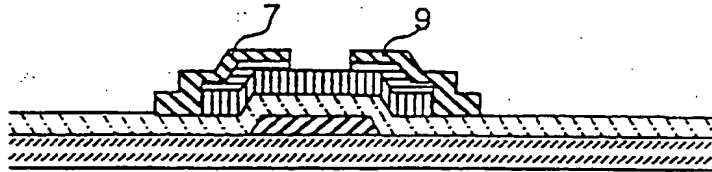


FIG. 18D

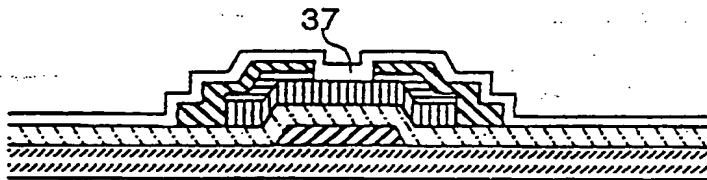
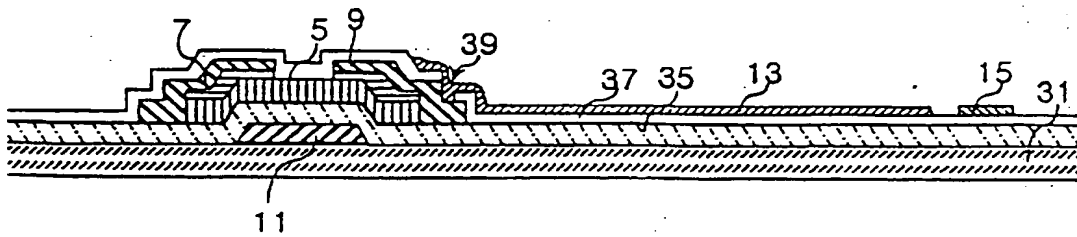


FIG. 18E



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FIG. 19A

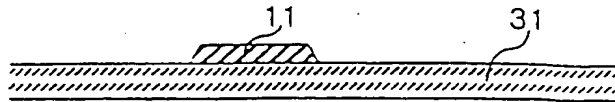


FIG. 19B

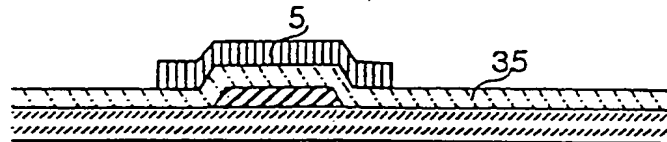


FIG. 19C

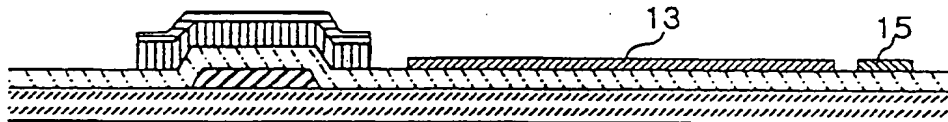


FIG. 19D

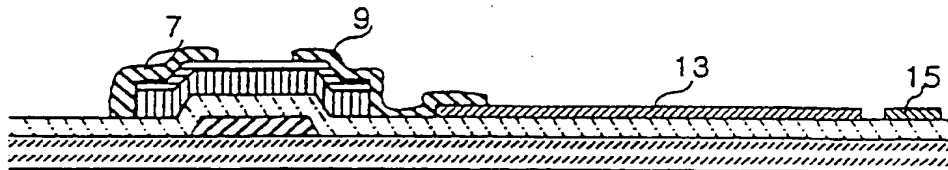


FIG. 19E

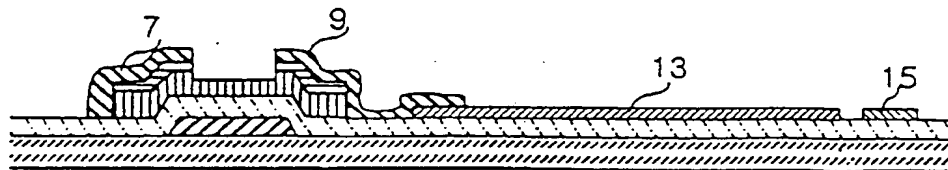
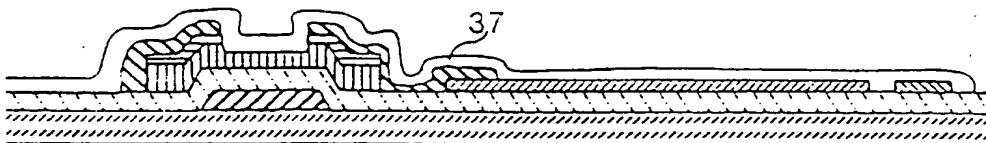


FIG. 19F



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FIG. 20A

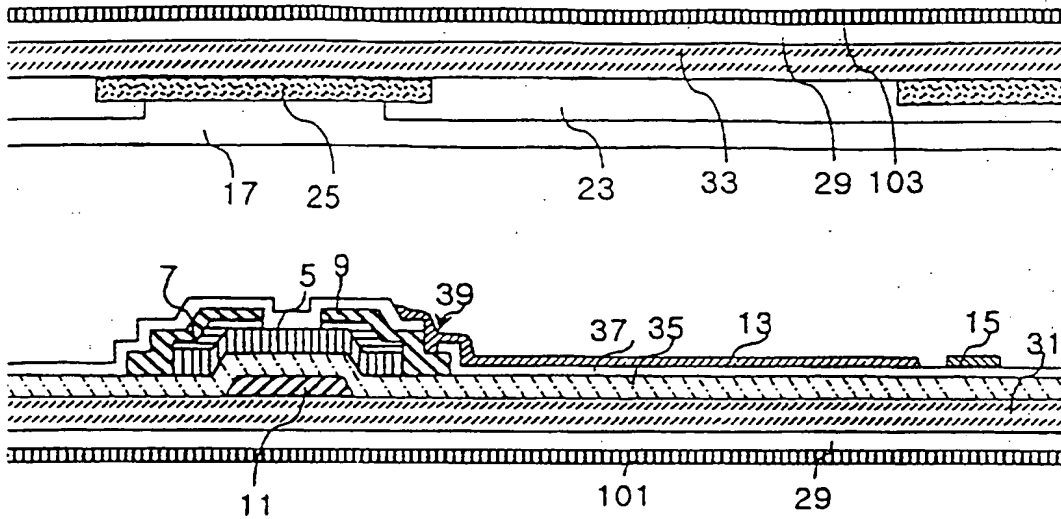
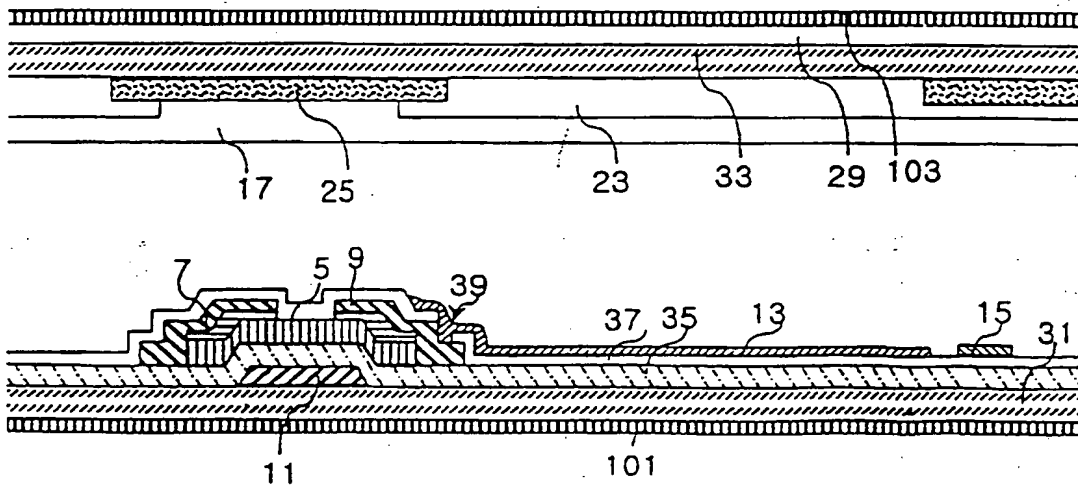


FIG. 20B



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FIG. 21A

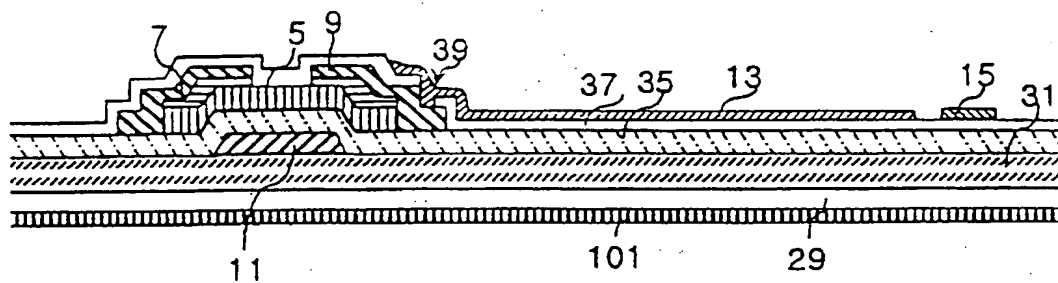
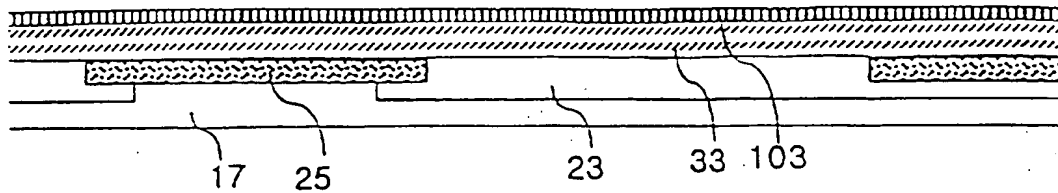
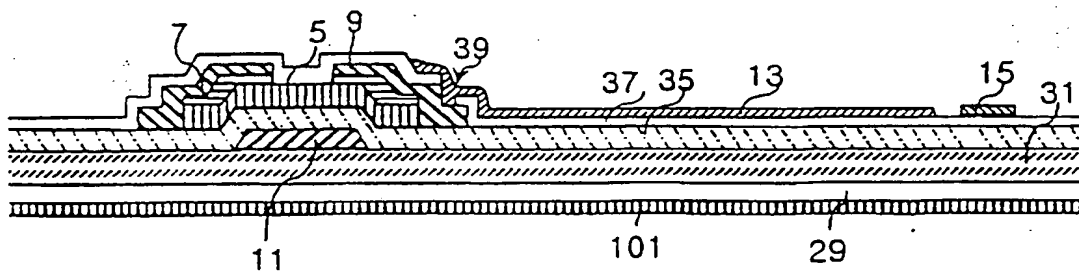
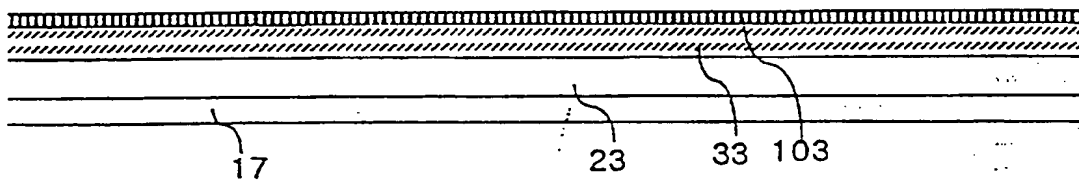


FIG. 21B



A MULTI-DOMAIN LIQUID CRYSTAL DISPLAY DEVICE

The present invention relates to a liquid crystal display device (LCD), and more particularly, to a multi-domain liquid crystal display device having a side or auxiliary electrode on a layer whereon a pixel electrode is formed.

Recently, an LCD has been proposed where the liquid crystal is not aligned and the liquid crystal is driven by side electrodes insulated from pixel electrodes. Figs. 1A to 1C are sectional views of pixel units of the conventional LCDs.

Regarding conventional LCDs, a plurality of gate bus lines arranged in a first direction on a first substrate and a plurality of data bus lines arranged in a second direction on the first substrate divide the first substrate into a plurality of pixel regions. A thin film transistor (TFT), applies image signal delivered from the data bus line to a pixel electrode 13 on a passivation 4.

The TFT is formed on each pixel region and comprises a gate electrode, a gate insulator, a semiconductor layer, a source electrode, and a drain electrode, etc. Side

electrode 15 is formed surrounding the pixel electrode 13 on the gate insulator, thereon passivation 4 is formed over the whole first substrate, and a part of pixel electrode 13 overlaps side electrode 15 (refer to Fig. 1A). Alternatively, pixel electrode 13 is formed on the gate insulator, passivation 4 is formed over the whole first substrate. Afterward, side electrode 15 is formed as overlapping a part of pixel electrode 13 (refer to Fig. 1B). Moreover, it is possible to pattern the pixel electrode 13 by etching and dividing the pixel region (refer to Fig. 1C).

On second substrate 33, a common electrode 17 is formed and together with pixel electrode 13 applies electric field to a liquid crystal layer. Side electrode 15 and open area (slit) 19 distort the electric field applied to the liquid crystal layer, and then in an unit pixel liquid crystal molecules are driven variously. This means that when voltage is applied to the LCD, dielectric energy due to the distorted electric field arranges the liquid-crystal directors in a needed position.

In the LCDs, however, open area 19 in common electrode 17 or pixel electrode 13 is necessary, and the

liquid crystal molecules could be driven stably when the open area is wider. If the electrodes do not have an open area or the width of the open area is narrow, the electric field distortion needed to divide the pixel region becomes weak. Then, when voltage over a threshold voltage, V_{th} , is applied, the time needed to stabilize the liquid crystal directors increases. In particular, the response time can be over 100msec. At this time, disclination occurs from the area where the liquid crystal directors are parallel with a transmittance axis of the polarizer, which results in a decrease in brightness. Further, according to the surface state of LCDs, the liquid crystal texture has an irregular structure.

Accordingly, the present invention is directed to an LCD that substantially obviates one or more problems due to limitations and disadvantages of the related art.

An object of the present invention is to provide a multi-domain LCD having rapid response characteristics and high brightness by stable arrangement of liquid crystal molecules.

Additional features and advantages of the invention

will be set forth in the description which follows, and in part will be apparent from the description, or may be learned by practice of the invention. The objectives and other advantages of the invention will be realized and attained by the structure particularly pointed out in the written description and claims hereof as well as the appended drawings.

To achieve the objects and in accordance with the purpose of the invention, as embodied and broadly described herein, a multi-domain liquid crystal display device comprises first and second substrates facing each other; a liquid crystal layer between said first and second substrates; a plurality of gate bus lines arranged in a first direction on said first substrate and a plurality of data bus lines arranged in a second direction on said first substrate to define a pixel region; a common electrode on said second substrate; a pixel electrode electrically charged through said data bus line to drive said liquid crystal layer with said common electrode; a side electrode on a same layer whereon said pixel electrode is formed; a passivation insulating said pixel and side electrodes from electrodes and bus lines other than the pixel and side electrodes;

and an alignment layer on at least one substrate between said first and second substrates.

In another aspect of the present invention, a multi-domain liquid crystal display device comprises first and second substrates facing each other; a liquid crystal layer between said first and second substrates; a plurality of gate bus lines arranged in a first direction on said first substrate and a plurality of data bus lines arranged in a second direction on said first substrate to define a pixel region; a common electrode on said second substrate; a pixel electrode coupled to the data bus line; a side electrode on a same layer as said pixel electrode; and a passivation insulating said pixel and side electrodes.

In another aspect of the present invention, a method of making a multi-domain liquid crystal display device comprises the steps of forming first and second substrates facing each other; forming a liquid crystal layer between said first and second substrates; forming a plurality of gate bus lines arranged in a first direction on said first substrate and a plurality of data bus lines arranged in a second direction on said first substrate to define a pixel region; forming a common electrode on said

second substrate; forming a pixel electrode electrically charged through said data bus line to drive said liquid crystal layer with said common electrode; forming a side electrode on a same layer whereon said pixel electrode is formed; forming a passivation insulating said pixel and side electrodes from electrodes and bus lines other than the pixel and side electrodes; and forming an alignment layer on at least one substrate between said first and second substrates.

In a further aspect of the present invention, a method of making a multi-domain liquid crystal display device comprises the steps of forming first and second substrates facing each other; forming a liquid crystal layer between said first and second substrates; forming a plurality of gate bus lines arranged in a first direction on said first substrate and a plurality of data bus lines arranged in a second direction on said first substrate to define a pixel region; forming a common electrode on said second substrate; forming a pixel electrode coupled to the data bus line; forming a side electrode on a same layer as said pixel electrode; and forming a passivation insulating said pixel and side electrodes.

It is to be understood that both the foregoing

general description and the following detailed description are exemplary and explanatory and are intended to provide further explanation of the invention as claimed.

Specific embodiments according to the invention will now be described, by way of example, with reference to the accompanying drawings, in which:

Figs. 1A to 1C are sectional views of the liquid crystal display devices in the related art;

Figs. 2A to 2D are plan views of the liquid crystal display devices according to the first embodiment of the present invention;

Figs. 3A to 3B are sectional views taken along the lines I-I¹ and II-II¹ of the liquid crystal display device in Fig. 2A;

Figs. 4A to 4C are plan views of the liquid crystal display devices according to the second embodiment of the present invention;

Figs. 5A to 5C are plan views of the liquid crystal display devices according to the third embodiment of the present invention;

Figs. 6A to 6D are plan views of the liquid crystal display devices according to the fourth embodiment of the present invention.

Figs. 7A to 7B are sectional views taken along the lines III-III¹ and IV-IV¹ of the liquid crystal display device in Fig. 6A;

Figs. 8A to 8C are plan views of the liquid crystal display devices according to the fifth embodiment of the present invention;

Figs. 9A to 9C are plan views of the liquid crystal display devices according to the sixth embodiment of the present invention.

Figs. 10A to 10D are plan views of the liquid crystal display devices according to the seventh embodiment of the present invention;

Figs. 11A to 11B are sectional views taken along the lines V-V¹ and VI-VI¹ of the liquid crystal display device in Fig. 10A;

Figs. 12A to 12C are plan views of the liquid crystal display devices according to the eighth

embodiment of the present invention;

Figs. 13A to 13C are plan views of the liquid crystal display devices according to the ninth embodiment of the present invention;

Figs. 14A to 14D are plan views of the liquid crystal display devices according to the tenth embodiment of the present invention.

Fig. 15 is a sectional view taken along the line VII-VII' of the liquid crystal display device in Fig. 14A;

Figs. 16A to 16C are plan views of the liquid crystal display devices according to the eleventh embodiment of the present invention;

Figs. 17A to 17C are plan views of the liquid crystal display devices according to the twelfth embodiment of the present invention;

Figs. 18A to 18E are sectional views of showing the manufacturing process of the present invention;

Figs. 19A-19F are sectional view of showing another manufacturing process of the present invention;

Figs. 20A and 20B shows the upper and lower substrates of the liquid crystal display of the present invention with different configurations of the compensation film; and

Figs. 21A and 21B show the upper and lower substrates of another embodiment in accordance with the present invention where Fig. 21A includes black matrix and Fig. 21B excludes black matrix.

The multi-domain liquid crystal display device comprises first and second substrates facing each other, a liquid crystal layer between the first and second substrates, a plurality of gate bus lines arranged in a first direction on the first substrate and a plurality of data bus lines arranged in a second direction on the first substrate to define a pixel region, a common electrode on the second substrate, a pixel electrode electrically charged with the data bus line to drive the liquid crystal layer with the common electrode, a side electrode on a layer whereon the pixel electrode is formed, a passivation below the pixel and side electrodes to insulate them from other electrodes and bus lines, and an alignment layer on at least one substrate between the first and second substrates.

The side electrode is in a region other than the region of the pixel electrode. The passivation includes a material selected from the group consisting of BCB

(benzocyclobutene), acrylic resin, polyimide compound, silicon nitride, and silicon oxide. The pixel region is divided into at least two portions. In each portion, the liquid crystal molecules in the liquid crystal layer are driven differently from each other. The alignment layer is divided into at least two portions. In each portion, liquid crystal molecules in the liquid crystal layer are aligned differently from each other.

At least one portion among the alignment layer portions is alignment-treated, or all portions of the alignment layer portions are non-alignment-treated. In this respect, at least one portion among the alignment layer portions may be rubbing-treated or photo-alignment-treated.

The liquid crystal layer includes liquid crystal molecules having positive dielectric anisotropy or liquid crystal molecules having negative dielectric anisotropy.

Hereinafter, a multi-domain liquid crystal display device is explained in detail by the accompanying drawings.

Figs. 2A to 2D are plan views of the liquid crystal display devices according to the first embodiment of the present invention. Figs. 2A to 2D shows a storage-on-

common structure but a storage-on-gate structure is also possible. The solid arrow shows the alignment direction of the upper substrate and the dotted arrow shows the alignment direction of the lower substrate.

In particular, Fig. 2A shows a four domain "+" shaped configuration where the alignment is in all four domains. Fig. 2B shows a four domain "x" shaped configuration where the alignment is in all four domains. Fig. 2C shows a four-domain configuration where the alignment is in only two of the four domains. Fig. 2D shows a two-domain configuration with no alignment.

Figs. 3A to 3B are sectional views taken along the lines I-I' and II-II' in Fig. 2A. Although not shown in Figs. 3A and 3B, one alignment layer may be on either the first or second substrate, or an alignment layer may be on each of the first and second substrates.

Referring to Figs. 2A-2D and 3A-3B, the device comprises a plurality of gate bus lines 1 arranged in a first direction on a first substrate and a plurality of data bus lines 3 arranged in a second direction on the first substrate, a TFT, a passivation 4, a pixel electrode 13, a side electrode 15, a common electrode 17 on a second substrate 20, and a liquid

crystal layer between the first and second substrates.

Data bus lines 3 and gate bus lines 1 divide the first substrate into a plurality of pixel regions. The TFT is formed on the each pixel region and comprises a gate electrode 11, a gate insulator, a semiconductor layer 5, an ohmic contact layer, and source/drain electrodes 7, 9. Passivation 4 is formed on the TFT and data bus lines 3 preferably through the whole first substrate. Pixel electrode 13 is coupled to drain electrode 9 and overlap the TFT and/or data bus lines 3, and gate bus lines 1 on passivation 4. Side electrode 15 is formed in a region except the region in which the pixel electrode is on passivation 4.

Side electrode 15 and pixel electrode 13 is separated by passivation 4, which prevents gate bus line 1 and data bus line 3 from interfering the distorted electric field of side electrode 15. The passivation 4 is formed by depositing a material such as SiN_x or SiO_x . Further, if side electrode 15 and pixel electrode 13 are overlapped with the TFT and/or data bus line 3, crosstalk is generated. Thus, side and pixel electrodes 15, 13 are formed to not overlap with the TFT and/or data bus line by the passivation as an interlayer.

To manufacture the LCD, in each pixel region on the first substrate, a TFT is formed comprising gate electrode 11, gate insulator, semiconductor layer 5, ohmic contact layer and source/drain electrodes 7, 9. At this time, a plurality of gate bus lines 1 and a plurality of data bus lines 3 are formed to divide the first substrate into a plurality of pixel regions.

Gate electrode 11, gate/data bus lines 1, 3, and source/drain electrodes 7, 9 are formed by depositing a metal such as Al, Mo, Cr, Ta or Al alloy, etc. using a sputtering and patterning technique. Semiconductor layer 5 and the ohmic contact layer are formed by depositing using Plasma CVD (Plasma Chemical Vapor Deposition) and patterning amorphous silicon (a-Si) and doped amorphous silicon (n^+ a-Si), respectively. The gate insulator is formed from depositing with Plasma CVD and patterning SiN_x or SiO_x . Side and pixel electrodes 15, 13 are formed by depositing a metal such as ITO (indium tin oxide), Al, Mo, Cr, Ta, Ti or Al alloy using a sputtering and patterning technique.

At this time, it is possible to form side and pixel electrodes 15, 13 by patterning the same metal once or by patterning different metals twice.

On second substrate 20, common electrode 17 is formed by depositing ITO through sputtering and patterning. The liquid crystal layer is formed by injecting liquid crystal between the first and second substrates.

The side electrode 15 preferably has the same or substantially the same potential as the common electrode.

One way to accomplish this is to electrically connect the side electrode 15 to the common electrode. For example, if the potential difference between the pixel electrode and the common electrode is 5V, then the potential difference between the pixel electrode and the side electrode is also 5V.

Furthermore, in the present LCD, an alignment layer(not shown in the figure) is formed over the whole first and/or second substrates. The alignment layer includes a material such as polyamide or polyimide based materials, PVA (polyvinylalcohol), polyamic acid or SiO_2 . When rubbing is used to determine an alignment direction, it should be possible to apply any material suitable for the rubbing treatment.

Moreover, it is possible to form the alignment layer with a photosensitive material such as PVCN

(polyvinylcinnamate), PSCN (polysiloxanecinnamate), and CelCN (cellulosecinnamate) based materials. Any material suitable for the photo-aligning treatment may be used. Irradiating light once on the alignment layer determines the alignment or pretilt direction and the pretilt angle. The light used in the photo-alignment is preferably a light in a range of ultraviolet light, and any of non-polarized light, linearly-polarized light, and partially-polarized light can be used.

In the rubbing or photo-alignment treatment, it is possible to apply one or both of the first and second substrates, and to apply different aligning-treatment on each substrate.

As shown in Figs. 4 and 5, from the aligning-treatment, a multi-domain LCD is formed with at least two domains, and LC molecules of the LC layer are aligned differently to one another on each domain. That is, as shown in Figs. 2A to 2D, the multi-domain is obtained by dividing each pixel into four domains such as in a "+" or "x" shape, or dividing each pixel horizontally, vertically, and/or diagonally, and differently alignment-treating or forming alignment directions on each domain and on each substrate.

The solid line arrow and dotted line arrow in the figures represent alignment directions of each domain of the first and second substrates, respectively. It is possible to have at least one domain of the divided domains unaligned. It is also possible to have all domains unaligned.

Figs. 4A to 4C are plan views of the liquid crystal display devices according to the second embodiment. The second embodiment is similar to the first embodiment, except that the side electrode 15 is formed on the side of only the data bus line 3, i.e., there is no horizontal side electrode. Therefore, the pixel region is broader than the first embodiment.

Further, the multi-domain is obtained by dividing each pixel into four domains such as in a "+" or "x" shape, or dividing each pixel horizontally, vertically, and/or diagonally, and differently alignment-treating or forming alignment directions on each domain and on each substrate. It is possible to have at least one domain of the divided domains unaligned. It is also possible to have all domains unaligned.

In particular, Fig. 4A shows a two-domain configuration where the alignment is in both domains.

Fig. 4B shows a four-domain configuration where the alignment is in only two of the four domains. Fig. 4C shows a two-domain configuration with no alignment.

Figs. 5A to 5C are plan views of the liquid crystal display devices according to the third embodiment. The third embodiment is similar to the first and second embodiments, except that the side electrode 15 is formed on the side of only the gate bus line 1, i.e., there is no vertical side electrode. Therefore, the pixel region may be broader than the first and second embodiments. The embodiments shown in Figs. 5A-5C have a storage-on-common structure.

Further, the multi-domain is obtained by dividing each pixel into four domains such as in a "+" or "x" shape, or dividing each pixel horizontally, vertically, and/or diagonally, and differently alignment-treating or forming alignment directions on each domain and on each substrate. It is possible to have at least one domain of the divided domains unaligned. It is also possible to have all domains unaligned.

Figs. 6A to 6D are plan views of the liquid crystal display devices according to the fourth embodiment.

Figs. 6A-6D are similar to Figs. 2A-2D, respectively,

except that the side electrode 15 overlaps the data bus line to increase the aperture ratio by using, for example, a low dielectric material such as BCB as the passivation.

Figs. 7A to 7B are sectional views taken along the lines III-III' and IV-IV' of the liquid crystal display device in Fig. 6A. Although not shown in Figs. 7A and 7B, one alignment layer may be on either the first or second substrate, or an alignment layer may be on each of the first and second substrates.

In the fourth embodiment, the passivation includes BCB (benzocyclobutene), acrylic resin, or polyimide compound, etc. as an organic insulator. Hence, the side electrode on the side of data bus line 3 is formed on any place on the data bus line 3 and the aperture ratio is improved. Moreover, the side electrode can be positioned in any place relative to the first three embodiments, and therefore, the pixel region is broader than the first, second, and third embodiments.

In the fourth embodiment, the multi-domain is obtained by dividing each pixel into four domains such as in a "+" or "x" shape, or dividing each pixel horizontally, vertically, and/or diagonally, and

differently alignment-treating or forming alignment directions on each domain and on each substrate. It is possible to have at least one domain of the divided domains unaligned. It is also possible to have all domains unaligned.

Figs. 8A to 8C are plan views of the liquid crystal display devices according to the fifth embodiment. The fifth embodiment is similar to the fourth embodiment, except that the side electrode 15 is formed on any place on only the data bus line 3. Therefore, the pixel region is broader than the fourth embodiment.

The multi-domain for the fifth embodiment is obtained by dividing each pixel into four domains such as in a "+" or "x" shape, or dividing each pixel horizontally, vertically, and/or diagonally, and differently alignment-treating or forming alignment directions on each domain and on each substrate. It is possible to have at least one domain of the divided domains unaligned. It is also possible to have all domains unaligned.

Figs. 9A to 9C are plan views of the liquid crystal display devices according to the sixth embodiment. The sixth embodiment is also similar to the fourth and fifth

embodiments, except that the side electrode 15 is formed any place on only the gate bus line 1 on the side where the TFT is formed on. The side electrode 15 is formed only along the side of the pixel electrode 13 which is parallel to the gate bus line 1, and not along the side which is parallel to the data bus line 3. Therefore, the aperture ratio is improved due to pixel electrode 13 overlapping data bus line 3 as compared to the fourth and fifth embodiments.

The multi-domain for the sixth embodiment is obtained by dividing each pixel into four domains such as in a "+" or "x" shape, or dividing each pixel horizontally, vertically, and/or diagonally, and differently alignment-treating or forming alignment directions on each domain and on each substrate. It is possible to have at least one domain of the divided domains unaligned. It is also possible to have all domains unaligned.

Referring to Figs. 10A to 10D and 11A and 11B, the device comprises a plurality of gate bus lines 1 arranged in a first direction on a first substrate 31 and a plurality of data bus lines 3 arranged in a second direction on the first substrate, a TFT, a passivation 37, a pixel electrode 13, and a side electrode 15.

On a second substrate 33, a light shielding layer 25 is formed to shield the light leaked from gate and data bus lines 1, 3, and the TFT, a color filter layer 23 is

formed on the light shielding layer, a common electrode 17 is formed on the color filter layer, and a liquid crystal layer is formed between the first and second substrates 31, 33.

Data bus lines 3 and gate bus lines 1 divide the first substrate into a plurality of pixel regions. The TFT is formed on the each pixel region and comprises a gate electrode 11, a gate insulator 35, a semiconductor layer 5, an ohmic contact layer, and source/drain electrodes 7, 9. Passivation 37 is formed through the whole first substrate. Pixel electrode 13 is coupled to drain electrode 9 and overlap data bus lines 3 or gate bus lines 1 on passivation 37. Side electrode 15 is formed in a region except the region in which pixel electrode 13 and the TFT are on passivation 37.

Side electrode 15 and pixel electrode 13 is separated by passivation 37, which prevents gate bus line 1 and data bus line 3 from interfering with the distorted electric field of side electrode 15. The passivation 37 is formed by depositing a material such as SiN_x or SiO_x . Further, if side electrode 15 and pixel electrode 13 are overlapped with the TFT and/or data bus line 3, crosstalk is generated. Therefore, side and pixel electrodes 15,

13 are formed not to overlap with the TFT and/or data bus line by the passivation as an interlayer.

For Figs. 10A-10D, the diagonal portion of the side electrode 15 may be replaced to form a perpendicular shape similar to Figs. 2A-2D, as an alternative embodiment.

Figs. 18A to 18E are sectional views of showing the manufacturing process of the present liquid crystal display device.

To manufacture the LCD, in each pixel region on the first substrate 31, a TFT is formed comprising gate electrode 11, gate insulator 35, semiconductor layer 5, ohmic contact layer and source/drain electrodes 7,9. At this time, a plurality of gate bus lines 1 and a plurality of data bus lines 3 are formed to divide the first substrate into a plurality of pixel regions.

Gate electrode 11 and gate bus line 1 are formed by, for example, sputtering and patterning a metal such as Al, Mo, Cr, Ta or Al alloy, as shown in Fig. 18A. The gate insulator 35 is formed on the gate electrode 11 by depositing SiN_x or SiO_x using, for example, PCVD (Plasma Chemical Vapor Deposition). Semiconductor layer 5 and

the ohmic contact layer are formed by depositing (through PCVD) and patterning amorphous silicon (a-Si) and doped amorphous silicon (n⁺-a-Si), respectively, as shown in Fig. 18B. Data bus line 3 and source/drain electrodes 7, 9 are formed by, for example, sputtering and patterning a metal such as Al, Mo, Cr, Ta or Al alloy, as shown in Fig. 18C.

Subsequently, passivation 37 is formed with BCB (BenzoCycloButene), acrylic resin, polyimide-based material, SiN_x or SiO_x over the whole first substrate, as shown in Fig. 18D. A contact hole 39 is formed to connect the pixel electrode to the drain electrode by opening a part of the passivation over the drain electrode 9. Thereafter, side and pixel electrodes 15, 13 are formed by, for example, sputtering and patterning a metal such as ITO (indium tin oxide), Al, Mo, Cr, Ta, Ti or Al alloy, as shown in Fig. 18E.

At this time, it is possible to form side and pixel electrodes 15, 13 by patterning the same metal once or by patterning different metals twice. When side electrode 15 and pixel electrode 13 are formed with the same material, the side and pixel electrodes are simultaneously formed with one mask. Alternatively, it

is possible to form the side and pixel electrodes with different materials or as a double layer with additional masks.

To apply a voltage (V_{com}) to side electrode 15, a silver (Ag) dot is preferably formed at each corner of the driving area on the first substrate 31. An electric field is applied with the second substrate 33 and the liquid crystal molecules are driven by the potential difference. The voltage (V_{com}) is applied to side electrode 15 by connecting the Ag dots to the side electrode, which may be accomplished simultaneously by forming the side electrode with ITO.

The side electrode 15 preferably has the same or substantially the same potential as the common electrode. One way to accomplish this is to electrically connect the side electrode 15 to the common electrode. For example, if the potential difference between the pixel electrode and the common electrode is 5V, then the potential difference between the pixel electrode and the side electrode is also 5V.

Moreover, when forming source/drain electrodes 7, 9 (see Fig. 18C), a storage electrode 43 is formed by, for example, sputtering and patterning a metal such as Al,

Mo, Cr, Ta, Ti or Al alloy which overlaps a part of gate bus line 1 (and/or side electrode 15) and pixel electrode 13 (see Fig. 11B). Storage electrode 43 is connected to pixel electrode 13 with contact hole 39, and forms storage capacitor 41 with gate bus line 1 (and/or side electrode 15).

Figs. 19A-19F show another method of manufacturing the liquid crystal display device of the present invention. In particular, the structure of the liquid crystal display in Figs. 19A-19F is not an IOP (ITO On Passivation) structure as shown in Figs. 18A-18E. Here, the pixel electrode (ITO) is formed prior to the passivation layer. Otherwise, the process steps are similar to Figs. 18A-18E.

Referring to Figs. 20A and 20B, on second substrate 33, a light shielding layer 25 is formed to shield light leakage from gate and data bus lines 1, 3, the TFT, and side electrode 15. A color filter layer 23 is formed with R, G, B (red, green, blue) elements on the light-shielding layer. A common electrode 17 is formed with ITO on the color filter layer 23. A liquid crystal layer is formed by injecting liquid crystal between the first and second substrates.

On at least one substrate, a compensation film 29 is formed with polymer. The compensation film may be a negative uniaxial film, which has one optical axis, and compensates the phase difference of the direction according to viewing-angle. Hence, it is possible to compensate effectively the right-left viewing-angle by widening the area without gray inversion, increasing contrast ratio in an inclined direction, and forming a multi-domain pixel.

In the present multi-domain liquid crystal display device, it is also possible to form a negative biaxial film as the compensation film, which has two optical axes and wider viewing-angle characteristics as compared with the negative uniaxial film. The compensation film may be formed on one of the substrates (such as shown in Fig. 20B) or on both substrates (such as shown in Fig. 20A).

After forming the compensation film 29 on at least one substrate, a polarizer is formed. At this time, the compensation film and polarizer are preferably composed as one.

Furthermore, in the present multi-domain LCD, an alignment layer (not shown in the figure) is formed over the whole first and/or second substrates 31, 33. The

alignment layer includes a material such as polyamide or polyimide based materials, PVA (polyvinylalcohol), polyamic acid or SiO_2 . When rubbing is used to determine an alignment direction, it should be possible to apply any material suitable for the rubbing treatment.

Moreover, it is possible to form the alignment layer with a photosensitive material such as PVCN (polyvinylcinnamate), PSCN (polysiloxanecinnamate), and CelCN (cellulosecinnamate) based materials. Any material suitable for the photo-aligning treatment may be used. Irradiating light once on the alignment layer determines the alignment or pretilt direction and the pretilt angle. The light used in the photo-alignment is preferably a light in a range of ultraviolet light. Any one of unpolarized light, linearly polarized light, and partially polarized light can be used.

In the rubbing or photo-alignment treatment, it is possible to apply one or both of the first and second substrates, and to apply different alignment treatment on each substrate.

From the alignment treatment, a multi-domain LCD is formed with at least two domains, and liquid crystal (LC) molecules of the LC layer are preferably aligned

differently from one another on each domain. That is, as shown in Figs. 10A to 10D, multi-domain is obtained by dividing each pixel into four domains such as in a "+" or "x" shape, or dividing each pixel horizontally, vertically, and/or diagonally, and differently alignment-treating or forming alignment directions on each domain and on each substrate.

The solid line arrow and dotted line arrow in the figures represent alignment directions of each domain of the first and second substrates, respectively. It is possible to have at least one domain of the divided domains unaligned. It is also possible to have all domains unaligned.

Figs. 12A to 12C are plan views of the liquid crystal display devices according to the eighth embodiment. The eighth embodiment is similar to the seventh embodiment, except that the side electrode 15 is formed on the side of only the data bus line 3. Therefore, the pixel region is broader than the seventh embodiment.

Further, the multi-domain is obtained by dividing each pixel into four domains such as in a "+" or "x" shape, or dividing each pixel horizontally, vertically,

and/or diagonally, and differently alignment-treating or forming alignment directions on each domain and on each substrate. It is possible to have at least one domain of the divided domains unaligned. It is also possible to have all domains unaligned.

Figs. 13A to 13C are plan views of the liquid crystal display devices according to the ninth embodiment. The ninth embodiment is similar to the seventh and eighth embodiments, except that the side electrode 15 is formed on the side of only the gate bus line 1. Therefore, the pixel region may be broader than the seventh and eighth embodiments.

Further, the multi-domain is obtained by dividing each pixel into four domains such as in a "+" or "x" shape, or dividing each pixel horizontally, vertically, and/or diagonally, and differently alignment-treating or forming alignment directions on each domain and on each substrate. It is possible to have at least one domain of the divided domains unaligned. It is also possible to have all domains unaligned.

Figs. 14A to 14D are plan views of the liquid crystal display devices according to the tenth embodiment, and Fig. 15 is a sectional view taken along

the lines VII-VII' of the liquid crystal display device in Fig. 14A.

In the tenth embodiment, the passivation 37 includes BCB (BenzoCycloButene), acrylic resin, or polyimide compound, etc. as an organic insulator. The passivation in this instance has a planar surface structure and has a low dielectric constant. Hence, the side electrode on the side of data bus line 3 may be formed on any place on the data bus line 3 and the aperture ratio is improved. Moreover, the side electrode can be positioned in any place relative to the former three embodiments, and therefore, the pixel region is broader than the seventh, eighth, and ninth embodiments.

In the tenth embodiment, the multi-domain is obtained by dividing each pixel into four domains such as in a "+" or "x" shape, or dividing each pixel horizontally, vertically, and/or diagonally, and differently alignment-treating or forming alignment directions on each domain and on each substrate. It is possible to have at least one domain of the divided domains unaligned. It is also possible to have all domains unaligned.

The storage capacitor in Figs. 14A-14D may be

similar to the storage capacitor disclosed with respect to Figs. 13A-13C.

Figs. 16A to 16C are plan views of the liquid crystal display devices according to the eleventh embodiment. The eleventh embodiment is similar to the tenth embodiment, except that the side electrode 15 is formed on any place on only the data bus line 3. Therefore, the pixel region is broader than the tenth embodiment.

The multi-domain for the eleventh embodiment is obtained by dividing each pixel into four domains such as in a "+" or "x" shape, or dividing each pixel horizontally, vertically, and/or diagonally, and differently alignment-treating or forming alignment directions on each domain and on each substrate. It is possible to have at least one domain of the divided domains unaligned. It is also possible to have all domains unaligned.

Figs. 17A to 17C are plan views of the liquid crystal display devices according to the twelfth embodiment. The twelfth embodiment is also similar to the tenth and eleventh embodiments, except that the side electrode 15 is formed any place on only the gate bus

line 1 on the side where the TFT is formed on. The side electrode 15 is formed only along the side of the pixel electrode 13 which is parallel to the gate bus line 1, and not along the side which is parallel to the data bus line 3. Therefore, the aperture ratio is improved due to pixel electrode 13 overlapping data bus line 3 as compared to the tenth and eleventh embodiments.

The multi-domain for the twelfth embodiment is obtained by dividing each pixel into four domains such as in a "+" or "x" shape, or dividing each pixel horizontally, vertically, and/or diagonally, and differently alignment-treating or forming alignment directions on each domain and on each substrate. It is possible to have at least one domain of the divided domains unaligned. It is also possible to have all domains unaligned.

The embodiment shown in Figs. 17A-17C is also similar to the embodiment shown in Figs. 13A-13C except that the side electrodes in Figs. 13A-13C are connected at the pixel region whereas the side electrodes in Figs. 17A-17C, although not shown, are connected at the periphery.

It is possible to apply the LCD of the present embodiments to any alignment and mode including, for example, (1) a homogeneous alignment where liquid crystal molecules in the liquid crystal layer are aligned

homogeneously to surfaces of the first and second substrates, (2) a homeotropic alignment where liquid crystal molecules in the liquid crystal layer are aligned homeotropically to surfaces of the first and second substrates, (3) a tilted alignment where liquid crystal molecules in the liquid crystal layer are aligned tiltedly to surfaces of the first and second substrates, (4) a twisted alignment where liquid crystal molecules in the liquid crystal layer are aligned twistedly to surfaces of the first and second substrates, and (5) a hybrid alignment where liquid crystal molecules in the liquid crystal layer are aligned homogeneously to surface of one substrate between the first and second substrates and are aligned homeotropically to surface of the other substrate.

Consequently, since the multi-domain LCD of the present embodiments forms the pixel electrode and the side electrode on the same layer, a high voltage is not needed to raise the intensity of the electric field applied between the two electrodes. Also, in the case of conducting an alignment-treatment, a rapid response time and a stable LC structure can be obtained by the formed pretilt and anchoring energy. Moreover, the disclination

is thus removed to thereby improve the brightness.

It will be apparent to those skilled in the art that various modifications can be made in the liquid crystal display device of the present invention without departing from the spirit or scope of the invention. Thus, it is intended that the present invention cover the modifications and variations of this invention provided they come within the scope of the appended claims and their equivalents.

CLAIMS:

1. A multi-domain liquid crystal display device comprising:
 - first and second substrates facing each other;
 - a liquid crystal layer between said first and second substrates;
 - a plurality of gate bus lines arranged in a first direction on said first substrate and a plurality of data bus lines arranged in a second direction on said first substrate to define a pixel region;
 - a common electrode on said second substrate;
 - a pixel electrode electrically charged through said data bus line to drive said liquid crystal layer with said common electrode;
 - an auxiliary electrode on a same layer whereon said pixel electrode is formed;
 - a passivation insulating said pixel and auxiliary electrodes from electrodes and bus lines other than the pixel and auxiliary electrodes; and
 - an alignment layer on at least one substrate between said first and second substrates.

2. The multi-domain liquid crystal display device according to claim 1, wherein said auxiliary electrode is in a region other than a region where said pixel electrode is formed.

3. The multi-domain liquid crystal display device according to claim 1 or 2, wherein said auxiliary electrode is electrically connected to said common electrode.

4. The multi-domain liquid crystal display device according to any of claims 1 to 3, wherein said pixel electrode includes a material selected from the group consisting of ITO (indium tin oxide), aluminum, and chromium.

5. The multi-domain liquid crystal display device according to any of claims 1 to 4, wherein said auxiliary electrode includes a material selected from the group consisting of ITO (indium tin oxide), aluminum, molybdenum, chromium, tantalum, titanium, and an alloy thereof.

6. The multi-domain liquid crystal display device according to any of claims 1 to 5, wherein said common electrode includes ITO (indium tin oxide).

7. The multi-domain liquid crystal display device according to any of claims 1 to 6, wherein said passivation includes a material selected from the group consisting of BCB (benzocyclobutene), acrylic resin, and polyimide compound.

8. The multi-domain liquid crystal display device according to any of claims 1 to 7, wherein said auxiliary electrode overlaps said data bus line.

9. The multi-domain liquid crystal display device according to any of claims 1 to 6, wherein said passivation includes a material selected from the group consisting of silicon nitride and silicon oxide.

10. The multi-domain liquid crystal display device according to any of claims 1 to 9, wherein said passivation insulates said auxiliary electrode from said pixel electrode.

11. The multi-domain liquid crystal display device according to any of claims 1 to 10, wherein said pixel region is divided into at least two portions, liquid crystal molecules in said liquid crystal layer in each portion being driven differently from each other.

12. The multi-domain liquid crystal display device according to any of claims 1 to 11, wherein said alignment layer is divided into at least two portions, liquid crystal molecules in said liquid crystal layer in each portion being aligned differently from each other.

13. The multi-domain liquid crystal display device according to claim 12, wherein at least one portion of said at least two portions of the alignment layer is alignment-treated.

14. The multi-domain liquid crystal display device according to claim 12, wherein all portions of said at least two portions of the alignment layer are non-alignment-treated.

15. The multi-domain liquid crystal display device according to claim 12, wherein at least one portion of said at least two portions of the alignment layer is rubbing-treated.

16. The multi-domain liquid crystal display device

according to claim 15, wherein said alignment layer includes a material selected from the group consisting of polyimide and polyamide based materials, PVA (polyvinylalcohol), polyamic acid, and silicon dioxide.

17. The multi-domain liquid crystal display device according to claim 12, wherein at least one portion of said at least two portions of the alignment layer is photo-alignment-treated.

18. The multi-domain liquid crystal display device according to claim 17, wherein said alignment layer includes a material selected from the group consisting of PVCN (polyvinylcinnamate), PSCN (polysiloxanecinnamate), and CelCN (cellulosecinnamate) based materials.

19. The multi-domain liquid crystal display device according to claim 17 or 18, wherein said alignment layer is photo-aligned by ultraviolet light.

20. The multi-domain liquid crystal display device according to any one of claims 17 to 19, wherein said alignment layer is photo-aligned by irradiating light at least one time.

21. The multi-domain liquid crystal display device according to any of claims 1 to 20, wherein said liquid crystal layer includes liquid crystal molecules having positive dielectric anisotropy.

22. The multi-domain liquid crystal display device according to any of claims 1 to 20, wherein said liquid crystal layer includes liquid crystal molecules having negative dielectric anisotropy.

23. The multi-domain liquid crystal display device according to any of claims 1 to 22, wherein liquid crystal molecules in said liquid crystal layer are aligned homogenously to surfaces of said first and second substrates.

24. The multi-domain liquid crystal display device according to any of claims 1 to 22, wherein liquid crystal molecules in said liquid crystal layer are aligned homeotropically to surfaces of said first and second substrates.

25. The multi-domain liquid crystal display device according to any of claims 1 to 22, wherein liquid crystal molecules in said liquid crystal layer are aligned tiltedly to surfaces of said first and second substrates.

26. The multi-domain liquid crystal display device according to any of claims 1 to 22, wherein liquid crystal molecules in said liquid crystal layer are aligned twistedly to surfaces of said first and second substrates.

27. The multi-domain liquid crystal display device according to any of claims 1 to 22, wherein liquid crystal molecules in said liquid crystal layer are aligned homogeneously to a surface of one substrate between said first and second substrates and are aligned homeotropically to a surface of the other substrate.

28. The multi-domain liquid crystal display device according to any of claims 1 to 27, wherein the passivation is below said pixel and auxiliary electrodes.

29. The multi-domain liquid crystal display device according to any of claims 1 to 28, wherein the auxiliary electrode is formed only on the data bus line side of the pixel electrode.

30. The multi-domain liquid crystal display device according to any of claims 1 to 28, wherein the auxiliary electrode is formed only on the gate bus line side of the pixel electrode.

31. The multi-domain liquid crystal display device according to any of claims 1 to 28, wherein the auxiliary electrode is formed on the data bus line side and the gate bus line side of the pixel electrode.

32. A multi-domain liquid crystal display device comprising:

- first and second substrates facing each other;

- a liquid crystal layer between said first and second substrates;

- a plurality of gate bus lines arranged in a first direction on said first substrate and a plurality of data bus lines arranged in a second direction on said first substrate to define a pixel region;

- a common electrode on said second substrate;

- a pixel electrode coupled to the data bus line;

- an auxiliary electrode on a same layer as said pixel electrode; and

- a passivation insulating said pixel and auxiliary electrodes.

33. The multi-domain liquid crystal display device according to claim 32, wherein the auxiliary electrode has a substantially the same potential as the common electrode.

34. A method of making a multi-domain liquid crystal display device having first and second substrates, the method comprising:

- forming a plurality of gate bus lines arranged in a first direction on said first substrate and a plurality of data bus lines arranged in a second direction on said first substrate to define a pixel region;

- forming a common electrode on said second substrate;
- forming a pixel electrode electrically charged through said data bus line to drive said liquid crystal layer with said common electrode;

- forming an auxiliary electrode on a same layer whereon said pixel electrode is formed;

- forming a passivation insulating said pixel and auxiliary electrodes from electrodes and bus lines other than the pixel and auxiliary electrodes;

- forming an alignment layer on at least one substrate

between said first and second substrates; and

forming a liquid crystal layer between said first and second substrates.

35. The method according to claim 34, wherein the auxiliary electrodes is formed only on the data bus line side of the pixel electrode.

36. The method according to claim 34, wherein the auxiliary electrode is formed only on the gate bus line side of the pixel electrode.

37. The method according to claim 34, wherein the auxiliary electrode is formed on the data bus line side and the gate bus line side of the pixel electrode.

38. The method according to any of claims 34 to 37, wherein the auxiliary electrode has a substantially at the same potential as the common electrode.

39. A method of making a multi-domain liquid crystal display device having first and second substrates, the method comprising:

forming a plurality of gate bus lines arranged in a

first direction on said first substrate and a plurality of data bus lines arranged in a second direction on said first substrate to define a pixel region;

forming a common electrode on said second substrate;

forming a pixel electrode coupled to the data bus line;

forming an auxiliary electrode on a same layer as said pixel electrode;

forming a passivation insulating said pixel and auxiliary electrodes; and

forming a liquid crystal layer between said first and second substrates.

40. The method according to claim 39, further comprising the step of forming an alignment layer on at least one substrate between said first and second substrates.

41. The method according to claim 39 or 40, wherein the auxiliary electrode is formed only on the data bus line side of the pixel electrode.

42. The method according to claim 39 or 40, wherein the auxiliary electrode is formed only on the gate bus line side of the pixel electrode.

43. The method according to claim 39 or 40, wherein the auxiliary electrode is formed on the data bus line side and the gate bus line side of the pixel electrode.

44. The method according to any of claims 39 to 43, wherein the auxiliary electrode has substantially the same potential as the common electrode.

45. A multi-domain liquid crystal display device comprising:

- first and second substrates facing each other;

- a liquid crystal layer between said first and second substrates;

- a plurality of gate bus lines arranged in a first direction on said first substrate and a plurality of data bus lines arranged in a second direction on said first substrate to define a pixel region;

- a common electrode on said second substrate;

- a pixel electrode electrically charged through said data bus line to drive said liquid crystal layer with said common electrode;

- an auxiliary electrode on a same layer whereon said pixel electrode is formed;

a passivation insulating said pixel and auxiliary electrodes from electrodes and bus lines other than the pixel and auxiliary electrodes; and

an alignment layer on at least one substrate between said first and second substrates, wherein

the auxiliary electrode has a substantially the same potential as the common electrode.

46. The multi-domain liquid crystal display device according to claim 45, wherein the auxiliary electrode is electrically connected to the common electrode.

47. A multi-domain liquid crystal display device comprising:

first and second substrates facing each other;

a liquid crystal layer between said first and second substrates;

a plurality of gate bus lines arranged in a first direction on said first substrate and a plurality of data bus lines arranged in a second direction on said first substrate to define a pixel region;

a common electrode on said second substrate;

a pixel electrode electrically charged through said

data bus line to drive said liquid crystal layer with said common electrode;

an auxiliary electrode including the same material as said pixel electrode, on a same layer whereon said pixel electrode is formed;

a passivation insulating said pixel and side electrodes from electrodes and bus lines other than the pixel and auxiliary electrodes; and

an alignment layer on at least one substrate between said first and second substrates.

48. A multi-domain liquid crystal display device comprising:

first and second substrates facing each other;

a liquid crystal layer between said first and second substrates;

a plurality of gate bus lines arranged in a first direction on said first substrate and a plurality of data bus lines arranged in a second direction on said first substrate to define a pixel region;

a common electrode on said second substrate;

a pixel electrode electrically charged through said data bus line to drive said liquid crystal layer with said

common electrode;

an auxiliary electrode including a different material what said pixel electrode includes, on a same layer whereon said pixel electrode is formed;

a passivation insulating said pixel and side electrodes from electrodes and bus lines other than the pixel and auxiliary electrodes; and

an alignment layer on at least one substrate between said first and second substrates.

49. A multi-domain liquid crystal display device comprising:

first and second substrates facing each other;

a liquid crystal layer between said first and second substrates;

a plurality of gate bus lines arranged in a first direction on said first substrate and a plurality of data bus lines arranged in a second direction on said first substrate to define a pixel region;

a common electrode on said second substrate;

a pixel electrode electrically charged through said data bus line to drive said liquid crystal layer with said common electrode;

an auxiliary electrode on a same layer whereon said pixel electrode is formed;

a passivation including a material selected from the group consisting of BCB (BenzoCycloButene), acrylic resin, and polyimide compound, and insulating said pixel and side electrodes from electrodes and bus lines other than the pixel and auxiliary electrodes; and

an alignment layer on at least one substrate between said first and second substrates.

50. A multi-domain liquid crystal display device comprising:

first and second substrates facing each other;

a liquid crystal layer between said first and second substrates;

a plurality of gate bus lines arranged in a first direction on said first substrate and a plurality of data bus lines arranged in a second direction on said first substrate to define a pixel region;

a common electrode on said second substrate;

a pixel electrode electrically charged through said data bus line to drive said liquid crystal layer with said common electrode;

an auxiliary electrode on a same layer whereon said pixel electrode is formed;

a passivation insulating said pixel and auxiliary electrodes from electrodes and bus lines other than the pixel and auxiliary electrodes; and

a compensation film on at least one substrate.

51. The multi-domain liquid crystal display device according to claim 50, wherein the compensation film includes a negative uniaxial film on an outer surface of the at least one substrate.

52. The multi-domain liquid crystal display device according to claim 50 or 51, further comprising an alignment layer on at least one substrate between said first and second substrates.

53. The multi-domain liquid crystal display device according to any of claims 50 to 52, further comprising a polarizer on at least one substrate between said first and second substrates.

54. The multi-domain liquid crystal display device according to claim 50, wherein the compensation film includes

a negative biaxial film on an outer surface of the at least one substrate.

55. A multi-domain liquid crystal display device comprising:

first and second substrates facing each other;

a liquid crystal layer between said first and second substrates;

a plurality of gate bus lines arranged in a first direction on said first substrate and a plurality of data bus lines arranged in a second direction on said first substrate to define a pixel region;

a thin film transistor comprising a gate electrode, a semiconductor layer, and source/drain electrodes, at a crossing area of said gate and data bus lines

a common electrode on said second substrate;

a pixel electrode electrically charged through said data bus line to drive said liquid crystal layer with said common electrode;

an auxiliary electrode on a same layer whereon said pixel electrode is formed except the area where said thin film transistor is formed;

a passivation insulating said pixel and auxiliary

electrodes from electrodes and bus lines other than the pixel and auxiliary electrodes; and

an alignment layer on at least one substrate between said first and second substrates.

56. A liquid crystal display device comprising:

first and second substrates;

a liquid crystal layer between said first and second substrates;

a plurality of gate bus lines arranged in a first direction on said first substrate and a plurality of data bus lines arranged in a second direction on said first substrate to define a pixel region;

a transistor having gate, source, and drain, the source being coupled to the data bus line, and the gate being coupled to the gate bus line;

a common electrode on said second substrate;

a pixel electrode electrically charged through said data bus line to drive said liquid crystal layer with said common electrode, the pixel electrode and the common electrode producing an electric field;

an auxiliary electrode on a same layer whereon a pixel electrode is formed to affect the electric field between

the pixel electrode and the common electrode; and

a layer insulating said pixel and auxiliary electrodes from electrodes and bus lines other than the pixel and auxiliary electrodes,

wherein said pixel region is divided into at least two portions and liquid crystal molecules in said liquid crystal layer in each of the at least two portions being electrically driven differently from each other.

57. A method of making a liquid crystal display device having first and second substrates, the method comprising the steps of:

forming a plurality of gate bus lines arranged in a first direction on said first substrate and a plurality of data bus lines arranged in a second direction on said first substrate to define a pixel region;

forming a transistor corresponding to the pixel region having gate, source, and drain electrodes;

forming a pixel electrode electrically charged through said data bus line to drive said liquid crystal layer with said common electrode;

forming an auxiliary electrode on a same layer whereon said pixel electrode is formed, the auxiliary

electrode and the pixel electrode being electrically insulated from each other;

forming a common electrode on the second substrate;
and

forming a liquid crystal layer between said first and second substrates,

wherein said pixel region is divided into at least two portions and liquid crystal molecules in said liquid crystal layer in each of the at least two portions capable of being electrically driven differently from each other.

58. A liquid crystal display device including an auxiliary electrode formed spaced apart from but on the same layer as a pixel electrode.

59. A liquid crystal display device substantially as hereinbefore described with reference to and/or substantially as illustrated in the accompanying drawings.

60. A method of making a liquid crystal display device substantially as hereinbefore described with reference to and/or substantially as illustrated in the accompanying drawings.



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Application No: GB 9912587.4
Claims searched: All

Examiner: Matthew Lincoln
Date of search: 25 August 1999

Patents Act 1977 Search Report under Section 17

Databases searched:

UK Patent Office collections, including GB, EP, WO & US patent specifications, in:

UK Cl (Ed.Q): G5C (CHE)

Int Cl (Ed.6): G02F 1/1343, 1/136

Other: Online: EPODOC, JAPIO, WPI

Documents considered to be relevant:

Category	Identity of document and relevant passage	Relevant to claims
X	EP 0814142 A2 (CANON) Claim 4 in particular	1, 32, 34, 39, 45, 49, 58 at least
X	JP 090230387 (CASIO) See PAJ vol. 098 no. 001	1, 29, 30, 32, 34, 39, 45, 49, 58 at least
X	US 5737051 (KONDO) Column 3 lines 14 to 33 in particular	1, 32, 34, 39, 45, 49, 55, 58 at least
X	US 5668650 (MORI) Whole document, figure 2 in particular	1, 32, 34, 39, 45, 49, 55, 58 at least
X	US 5574582 (TAKEDA) Claim 1 in particular	1, 32, 34, 39, 45, 49, 55, 58 at least
X	US 5517342 (KIM) Column 18 lines 37 to 61, figure 13	1, 32, 34, 39, 45, 49, 58 at least

X Document indicating lack of novelty or inventive step
Y Document indicating lack of inventive step if combined with one or more other documents of same category.
& Member of the same patent family

A Document indicating technological background and/or state of the art.
P Document published on or after the declared priority date but before the filing date of this invention.
E Patent document published on or after, but with priority date earlier than, the filing date of this application.



The
Patent
Office



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Application No: GB 9912587.4
Claims searched: All

Examiner: Matthew Lincoln
Date of search: 25 August 1999

Category	Identity of document and relevant passage	Relevant to claims
X	US 4728175 (BARON) Column 5 line 50 to column 6 line 33, figures 1 and 5.	1, 32, 34, 39, 45, 49, 58 at least

X	Document indicating lack of novelty or inventive step	A	Document indicating technological background and/or state of the art.
Y	Document indicating lack of inventive step if combined with one or more other documents of same category.	P	Document published on or after the declared priority date but before the filing date of this invention.
&	Member of the same patent family	E	Patent document published on or after, but with priority date earlier than, the filing date of this application.



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Application No: GB 9929051.2

Page 2

27 March 2000

Amendment/withdrawal

If you wish to file amended claims for inclusion with the published application, or to withdraw the application to prevent publication, you must do so before the preparations for publication are completed. **No reminder will be issued.** If you write to the Office less than 3 weeks before the above completion date, please mark your letter prominently: **"URGENT - PUBLICATION IMMINENT"**.

Yours faithfully

Tony Reeve
Examiner

Important information about combined search and examination

I also ask that you take note of the following points. These might have a bearing on the future stages of your application because the examination report has been sent to you before your application has been published.

- (a) You may file voluntary amendments before making a full response to my examination report. We will publish with your application any new or amended claims you file voluntarily or as a full response, provided that they are received before preparations for publication are completed. It would help us when you file amendments before publication if you could **prominently indicate** in a covering letter whether or not the amendments are intended as a full response to the examination report.
- (b) If you file a full response to the examination report before your application is published I will consider it as soon as possible. However, if this would disrupt the publication of your application, I would have to delay taking any action until the application had been published. This delay could be up to 3 months, depending upon when we receive your response.
- (c) There is another situation when there might be a delay between you filing a full response and the Patent Office responding to it. This would arise if you met all my objections but your application had not or had only recently been published. I could not report the outcome of my re-examination until I was satisfied that the search was complete for documents published before the priority date of your invention and that anybody interested in the application has had three months following publication of the application to make observations on the patentability of your invention.
- (d) Provided that the requirements of the Act have been met, I can send your application to grant as early as three months after publication. Before doing so I will bring the original search up to date and raise with you any further objection that might result from this top-up search. However, there is a possibility that at that time I may not have access to all the patent applications published after the priority date of your invention and of possible relevance to your application. If this is the case I would have to complete the search after grant and if necessary raise any new found novelty objection then.



INVESTOR IN PEOPLE

Application No: GB 9929051.2
Claims searched: 1 - 37

Examiner: Tony Reeve
Date of search: 23 March 2000

Patents Act 1977 Search Report under Section 17

Databases searched:

UK Patent Office collections, including GB, EP, WO & US patent specifications, in:

UK Cl (Ed.R): G5C (CHE, CHG)

Int Cl (Ed.7): G02F 1/136

Other: Online: EPODOC; WPI; PAJ

Documents considered to be relevant:

Category	Identity of document and relevant passage	Relevant to claims
X	GB 2337843 A (KIM et al.) - see fig.6A, p.12 lin.17 to p.13 lin.23, p.18 lin.21 to p.19 lin.4	1,33,34 at least
X	US 5777701 (ZHANG) - see figs.1 & 2, col.3 lin.53 to col.4 lin.67	33
X	JP 050297412 A (YASUMITSU et al.) - see figs.1 & 4, and abstract	33

X	Document indicating lack of novelty or inventive step	A	Document indicating technological background and/or state of the art.
Y	Document indicating lack of inventive step if combined with one or more other documents of same category.	P	Document published on or after the declared priority date but before the filing date of this invention.
&	Member of the same patent family	E	Patent document published on or after, but with priority date earlier than, the filing date of this application.



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Your ref: MCF/amb/P14941
Application No: GB 9929051.2
Applicant: LG. Philips LCD Co., Ltd.

Examiner: Tony Reeve
Tel: 01633 814580
Date of report: 27 March 2000

Latest date for reply: 11 December 2000

Page 1/2

Patents Act 1977

Combined Search and Examination Report under Sections 17 & 18(3)

Novelty (Section 1(1)(a))

1. The invention as defined in claim 33 is not new because it has already been disclosed in each of the following documents:

- a) GB 2337843 A (KIM et al.) - see fig.6A, p.12 lin.17 to p.13, p.18 lin.21 to p.19 lin.4
- b) US 5777701 (ZHANG) - see figs.1 & 2, col.3 lin.53 to col.4 lin.67
- c) JP 050297412 A (YASUMITSU et al.) - see figs.1 & 4, and abstract

2. All of the cited documents describe active matrix liquid crystal devices and in particular the arrangement of the pixel elements. Thus, as is standard to such devices, there are two facing substrates with pixel electrodes and common electrodes on opposing faces and orthogonal gate and data bus lines in between. The three cited documents also have an extra auxiliary electrode surrounding the pixel electrode and therefore these documents anticipate claim 33.

Inventive step (Section 1(1)(b))

3. The invention as defined in claims 1 and 34 at least is obvious in view of what has already been disclosed in the following document:

GB 2337843 A (KIM et al.) - see fig.6A, p.12 lin.17 to p.13, p.18 lin.21 to p.19 lin.4

4. In addition to the features mentioned above, this document describes a device containing a passivation layer and alignment layers, thus, when combined with features well known in the art of active matrix display construction, this document shows a lack of inventive step for claims 1 and 34 at least.

Clarity, Consistency and Scope of Invention (Section 14(5))

5. The dependent claims 10 and 11 are identical, as are the dependent claims 30 and 31. Thus, for conciseness one of the identical pair should be modified or removed.

6. Claim 17 refers to "...an overcoat layer on said color filter layer.". It is not clear what the 'overcoat layer' actually is. Are you perhaps referring to common electrode layer or to the substrate? Note that if the overcoat layer does refer to the common electrode then



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Your Reference: MCF/amb/P14941
Application No: GB 9929051.2

27 March 2000

Dear Sirs

Patents Act 1977:
Combined Search and Examination Report under Sections 17 and 18(3)

Latest date for reply: 11 December 2000

I enclose two copies of my search and examination report and a copy of the citations.

By the above date you should either file amendments to meet the objections in the report or make observations on them. If you do not, the application may be refused.

Publication

I estimate that, provided you have met all formal requirements, preparations for publication of your application will be completed soon after 2 May 2000. You will then receive a letter informing you of completion and telling you the publication number and date of publication.

†Use of E-mail: Please note that under patent law e-mail may be used to file correspondence only.



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Your ref: MCF/amb/P14941
Application No: GB 9929051.2

Date of Report: 27 March 2000
Page 2/2

[Examination Report contd.]

claim 18 becomes identical to claim 15 and for conciseness one of these claims should be either modified or removed.

7. Claim 25 mentions for the first time that "...said alignment layer is divided into at least two portions,...". Claim 26 refers to "...one portion of said at least two portions..." and therefore must be appendant to claim 25, however, claim 26 states "...according to any one of claims 1 to 25..." which is inconsistent.

8. In claim 34 it is not understood what the term "...n-line..." means with respect to the thin film transistor.

9. Whilst the six embodiments of the invention are apparent from the description, there is no mention in the claims of the separate embodiments. Therefore, claim 35 which refers to "...the first to sixth embodiments..." is not clear in meaning.

10. The statement of invention on page 4 appears to correspond to claim 1 only and therefore does not provide support for the independent claims 33 and 34.